

FIG. 1A

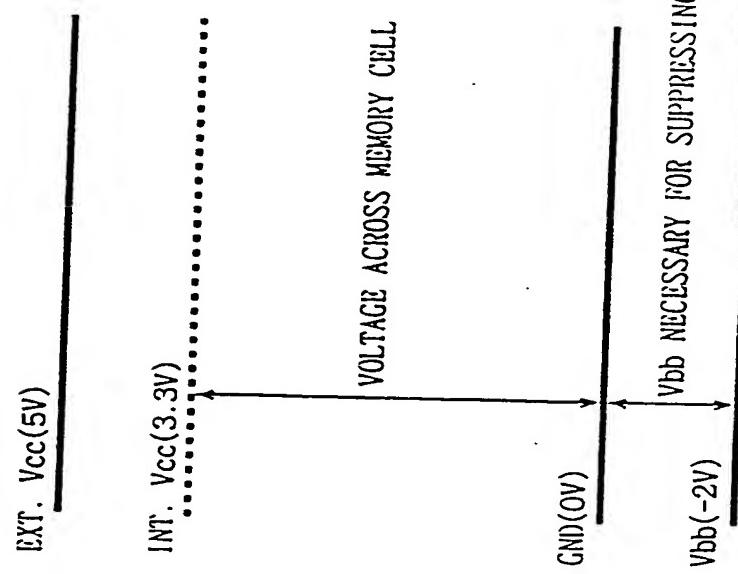


FIG. 1B

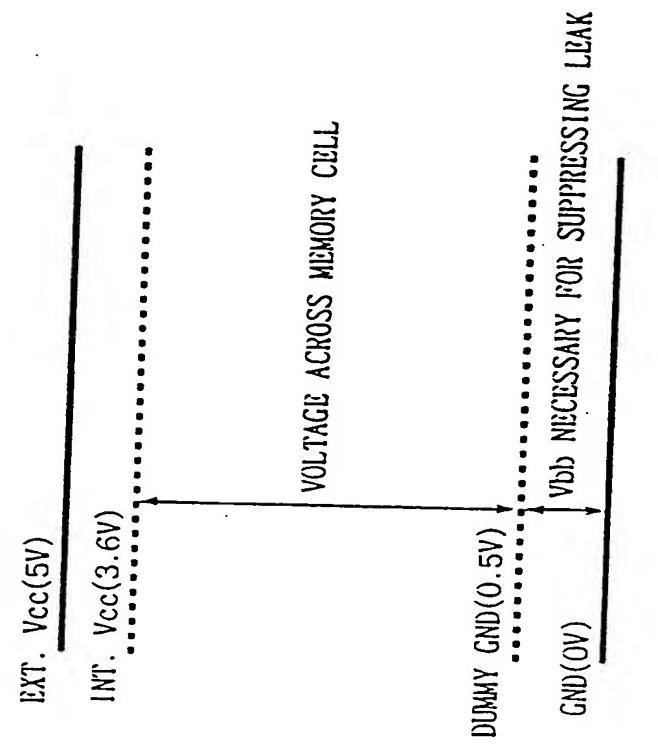
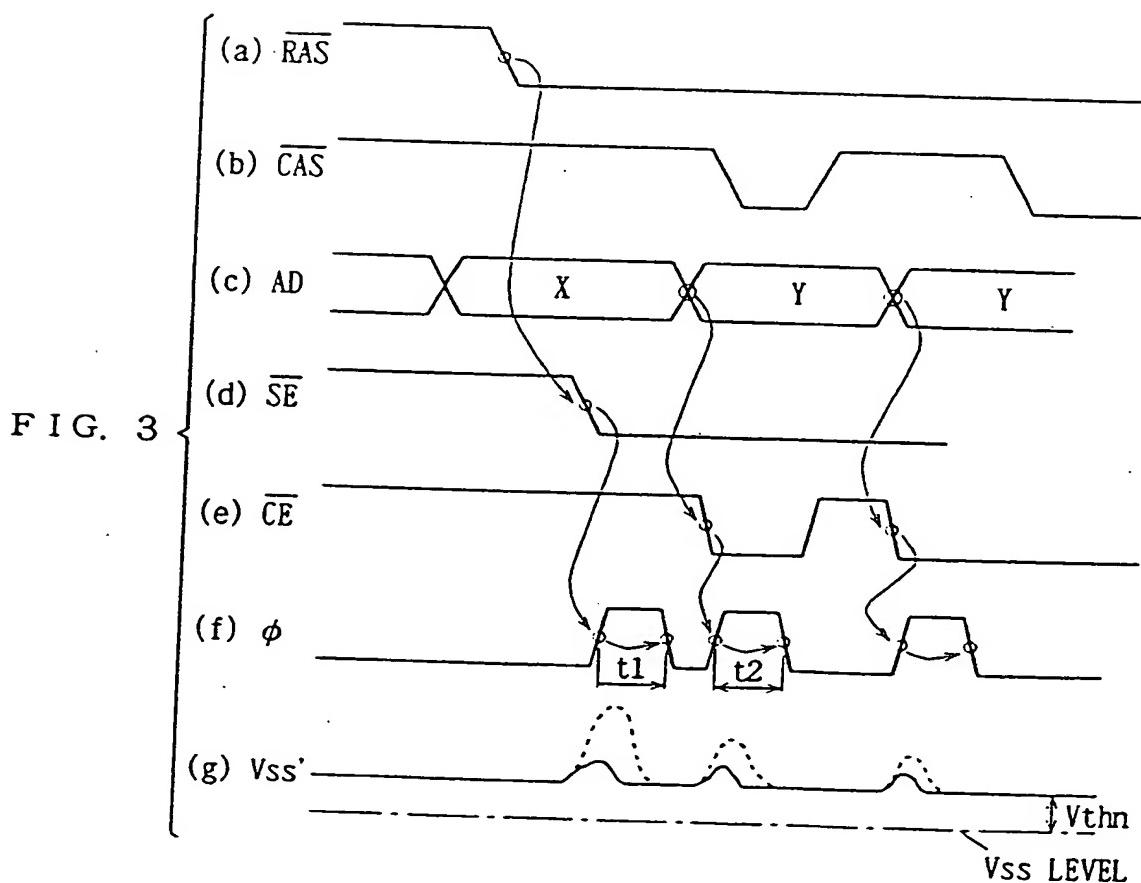
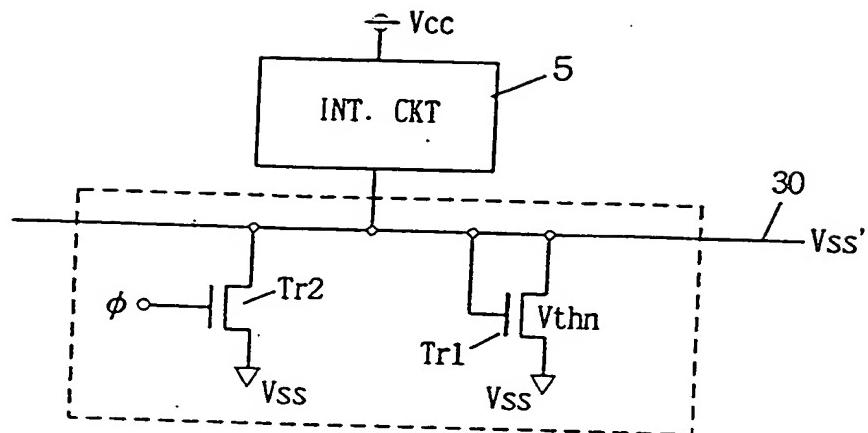
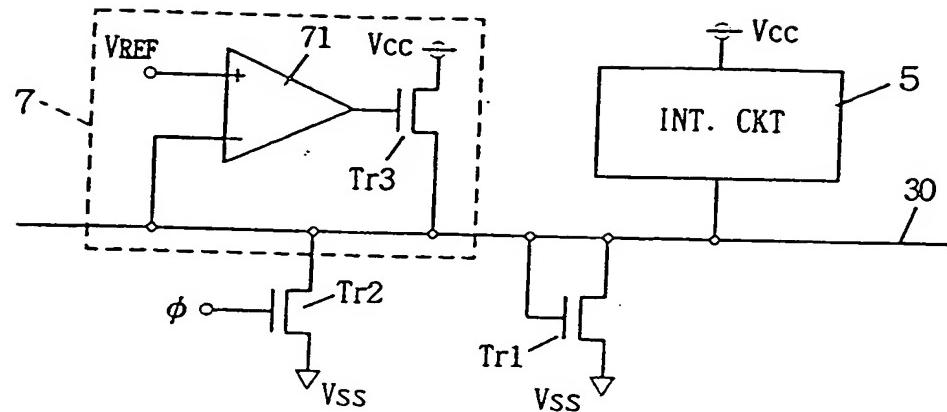


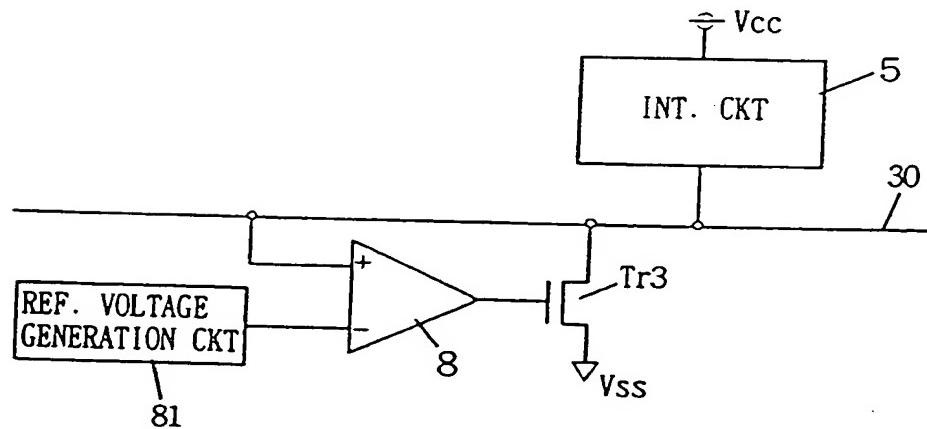
FIG. 2



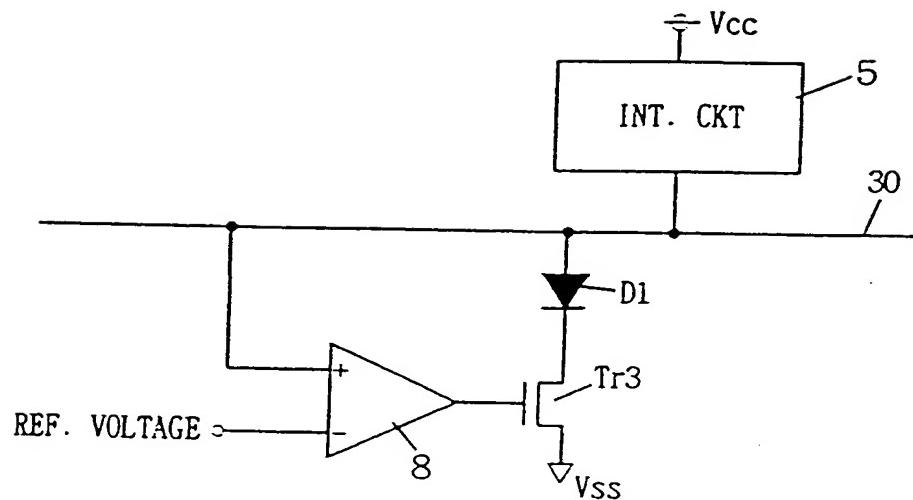
F I G. 4



F I G. 5



F I G. 6



F I G. 7

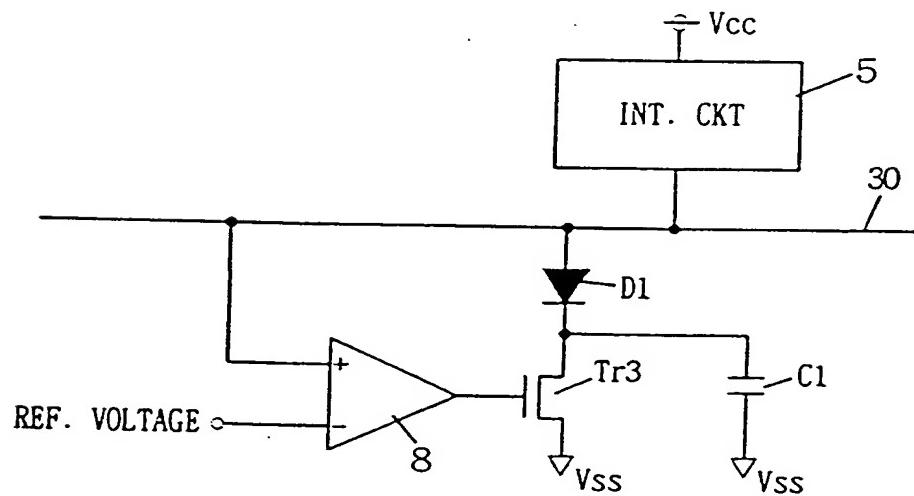
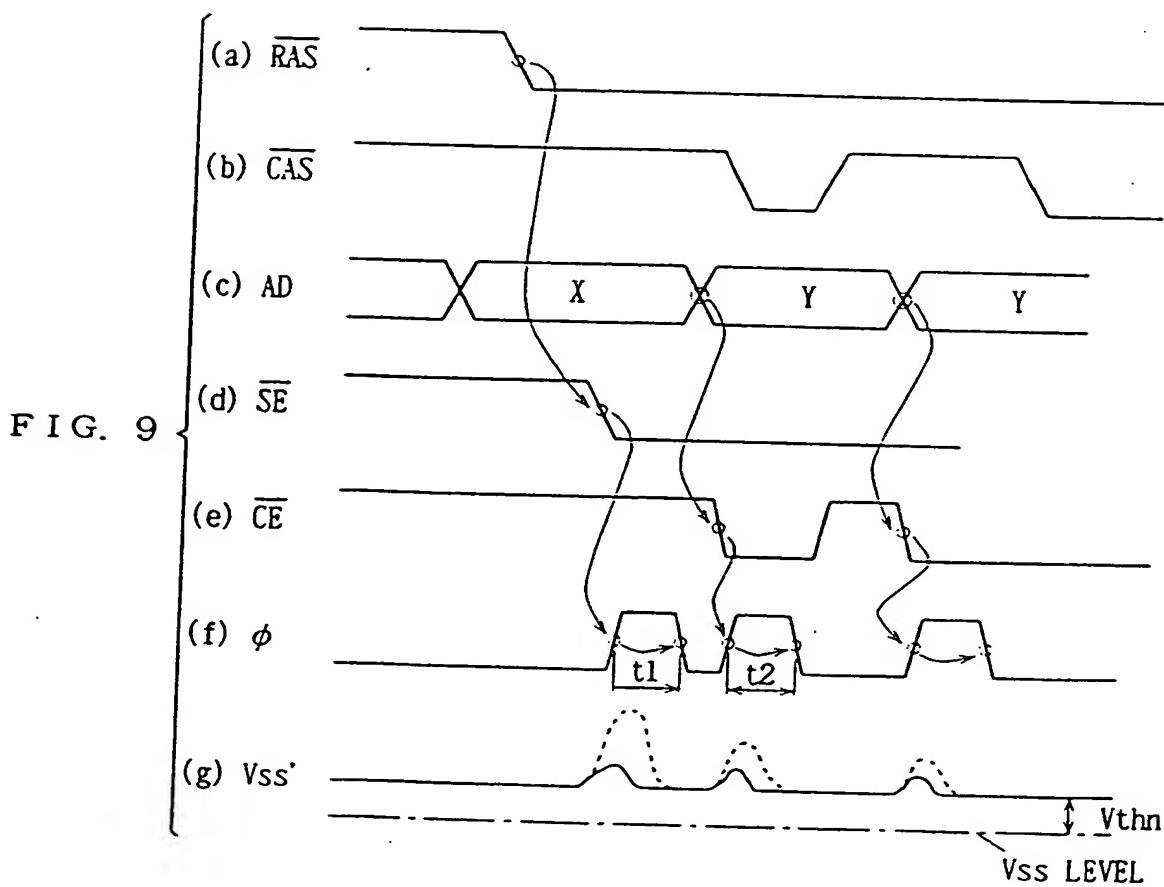
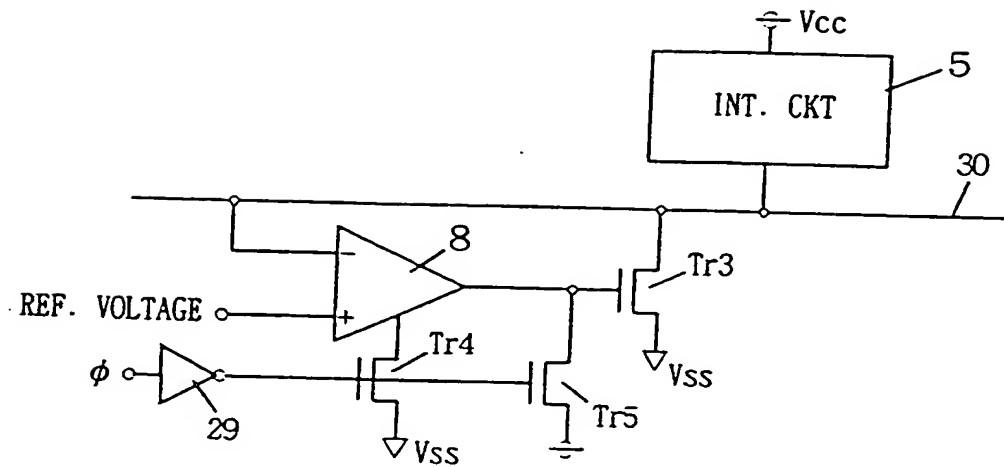
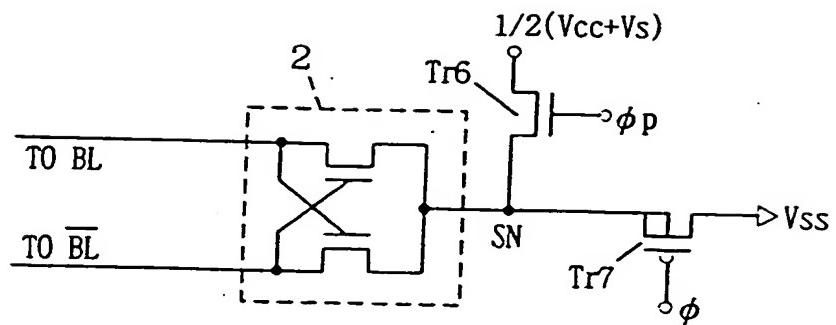


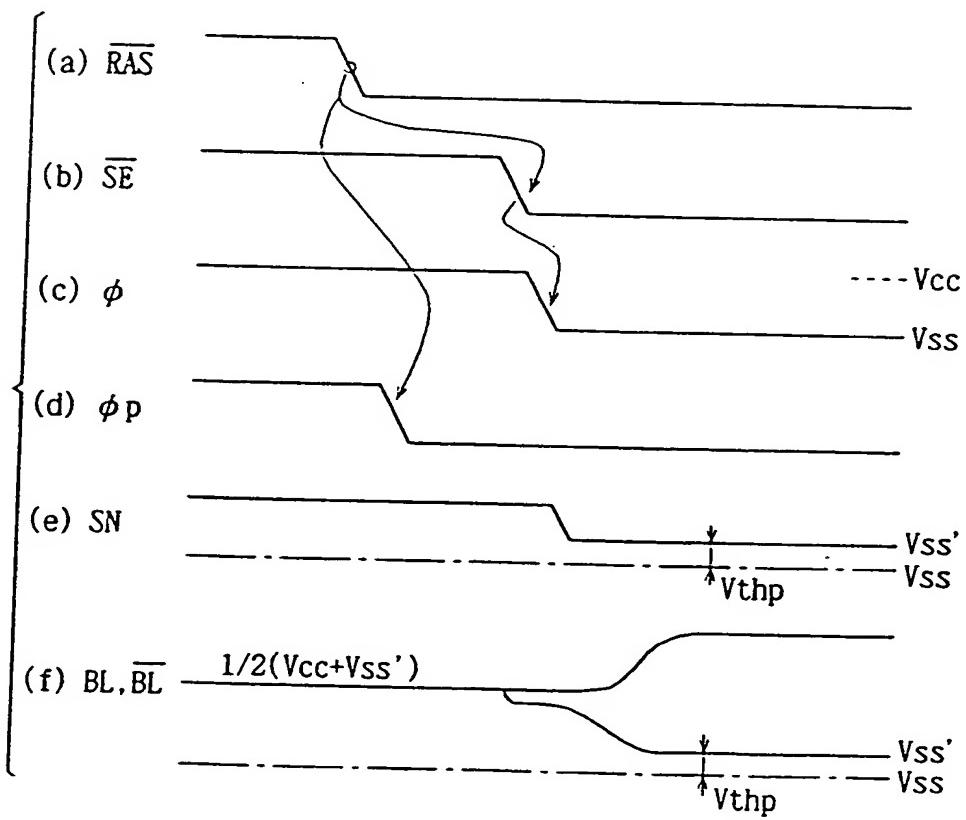
FIG. 8



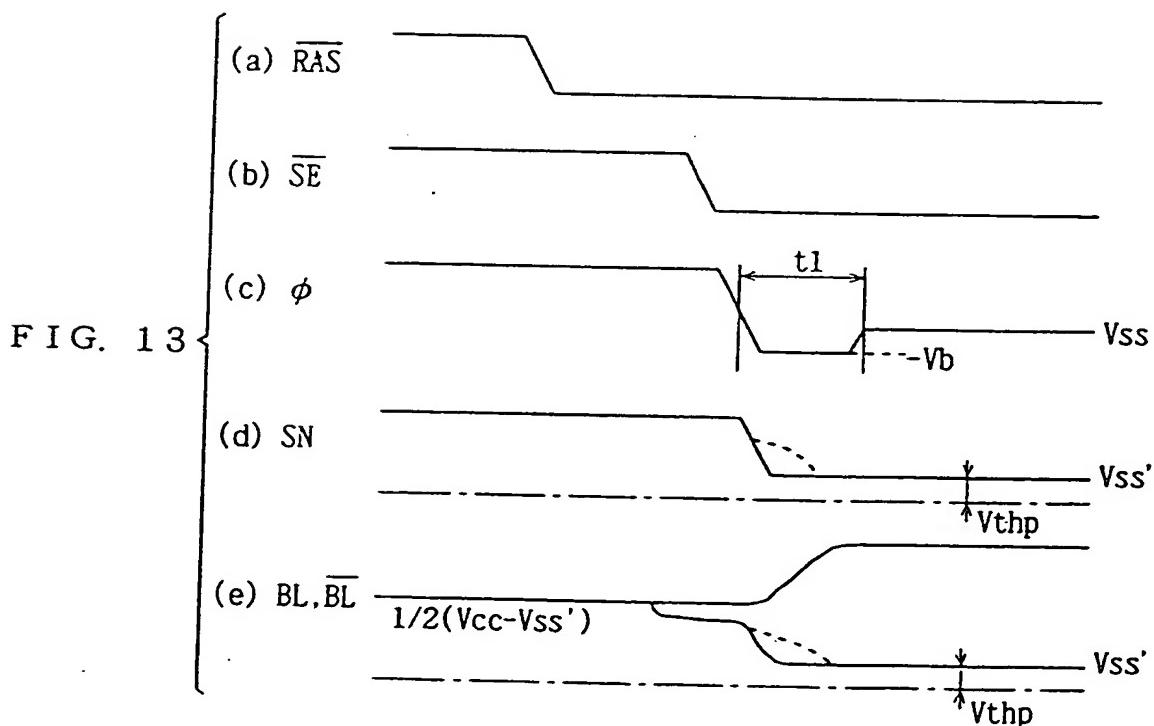
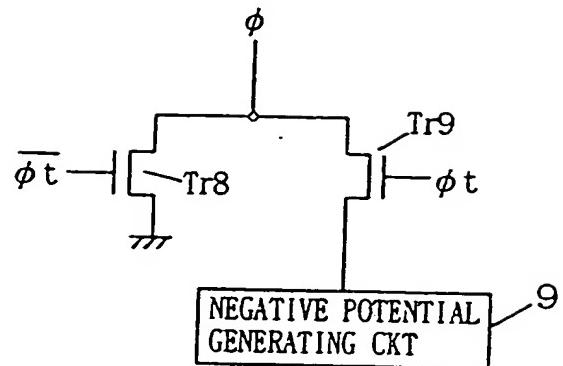
F I G. 10



F I G. 11



F I G. 12



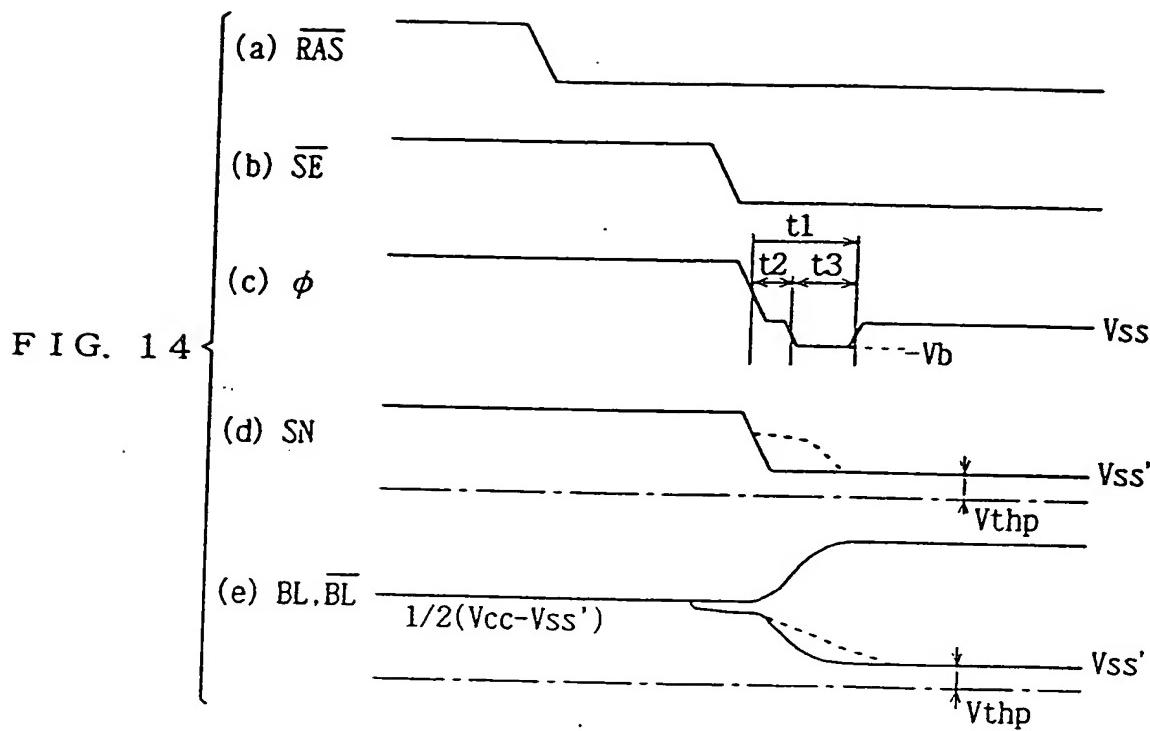


FIG. 15

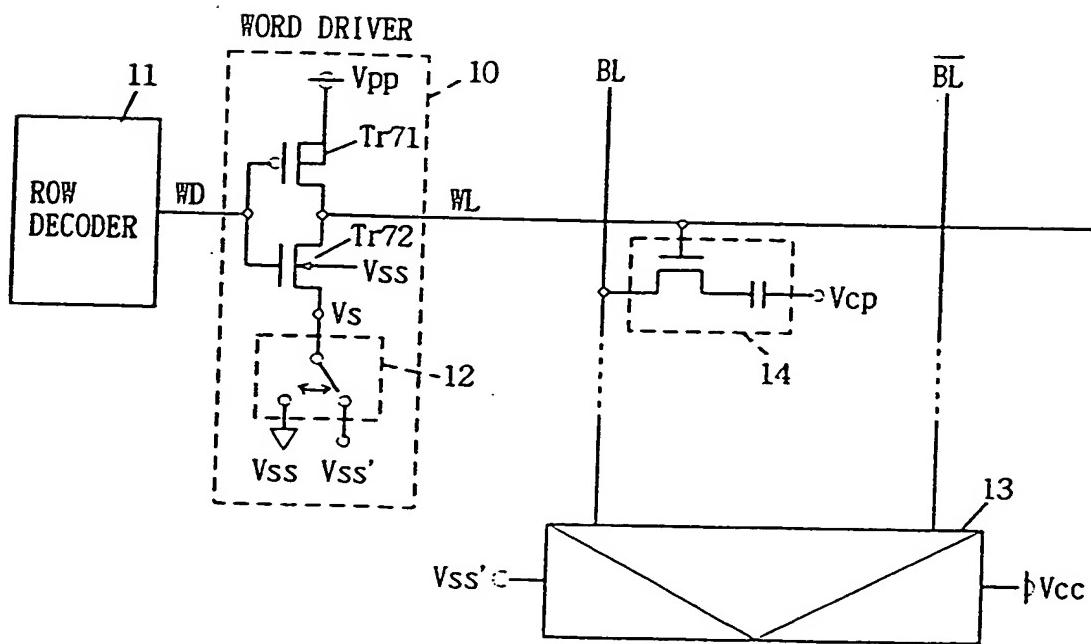


FIG. 16

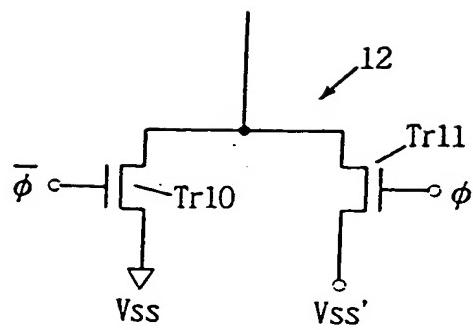
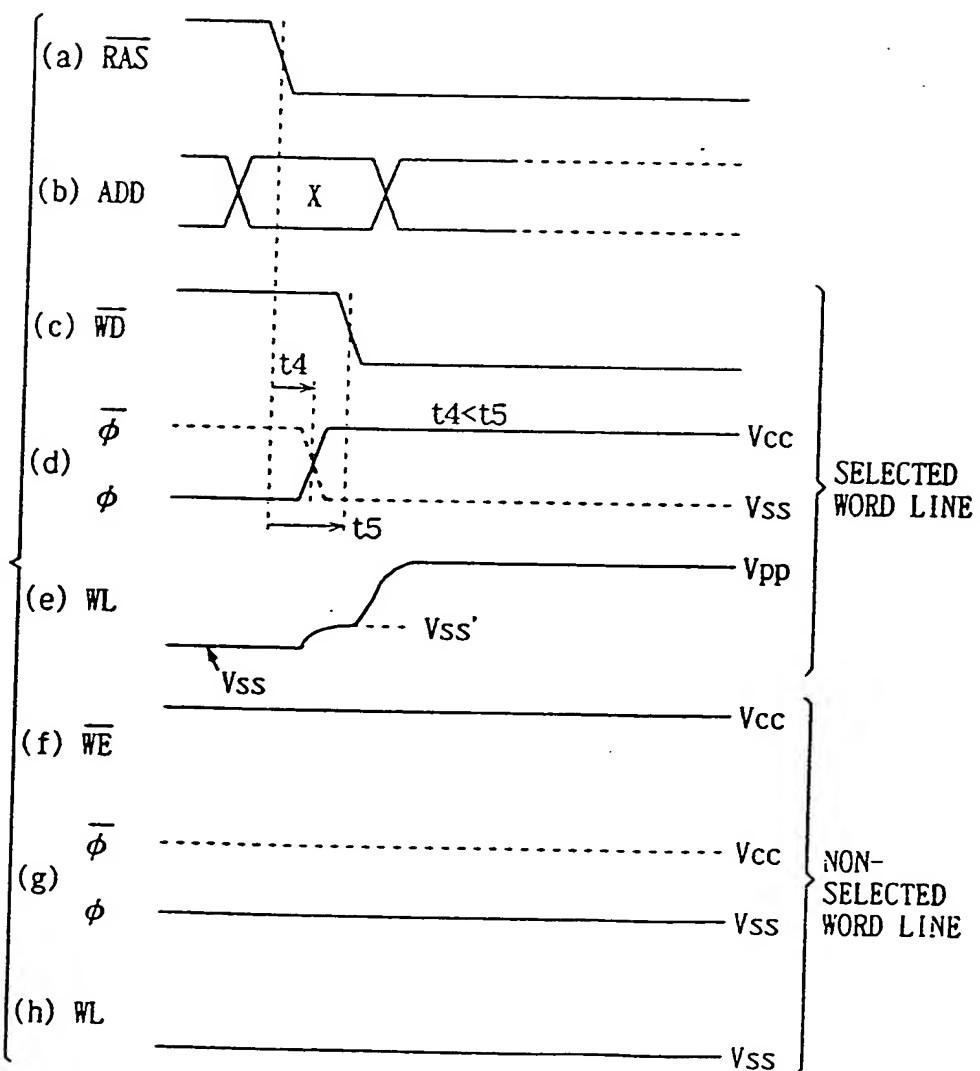
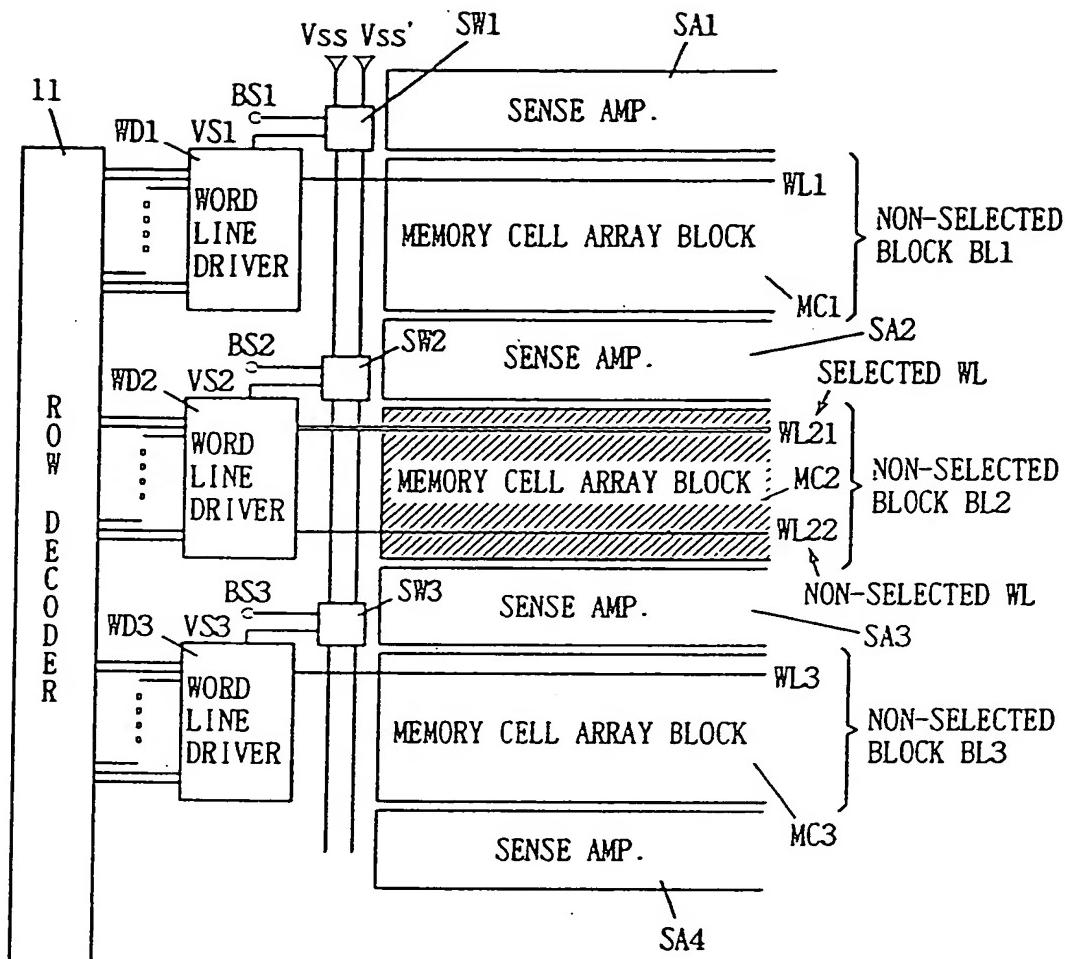


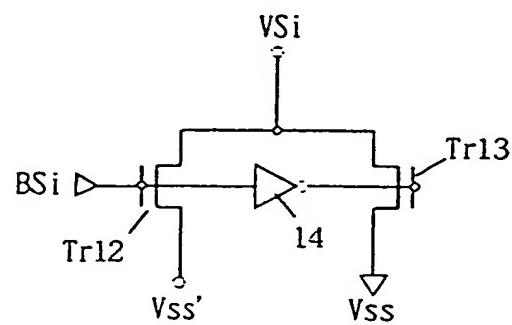
FIG. 17

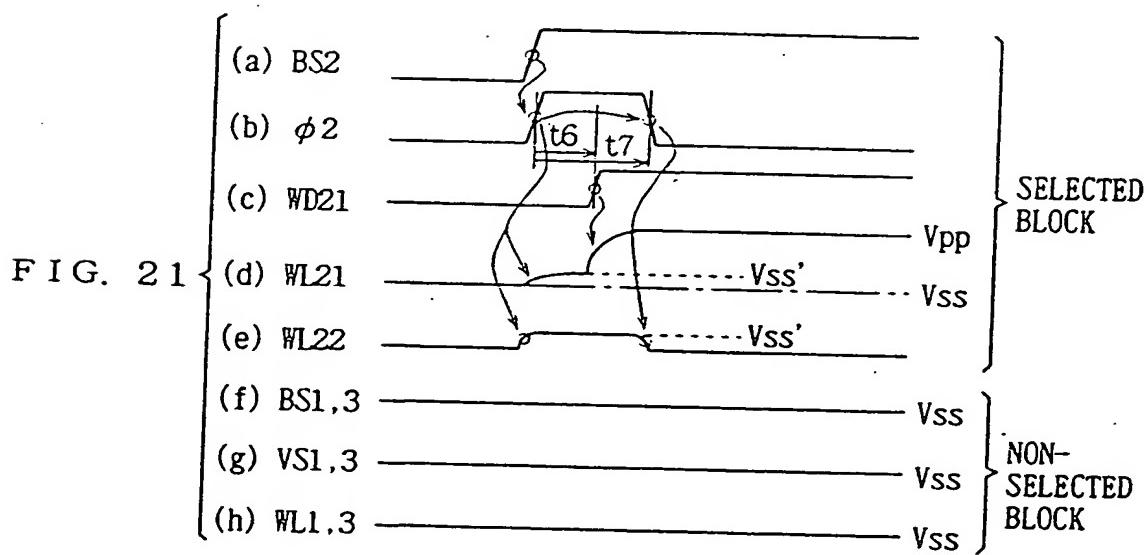
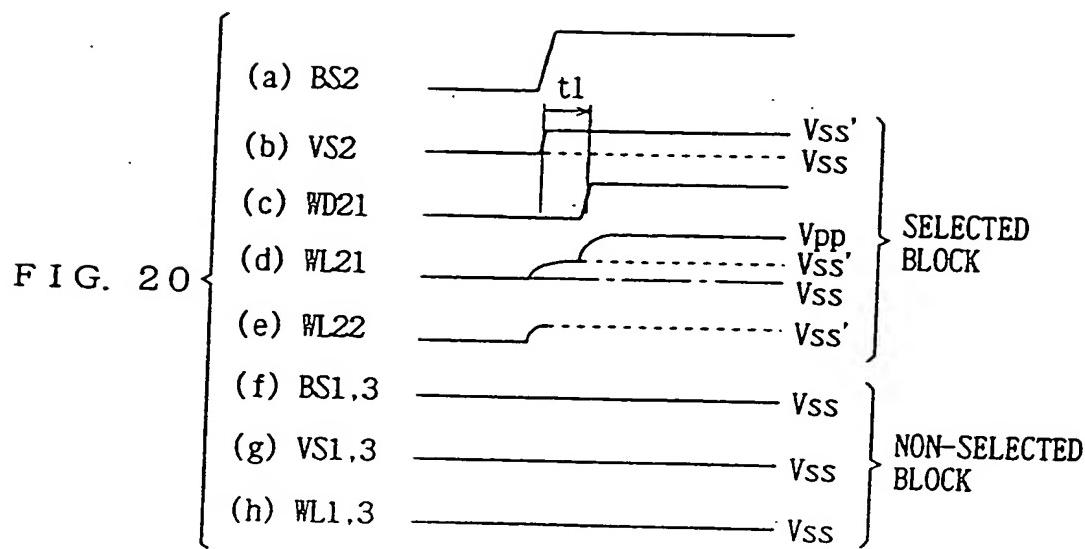


F I G. 18



F I G. 19





F I G. 22

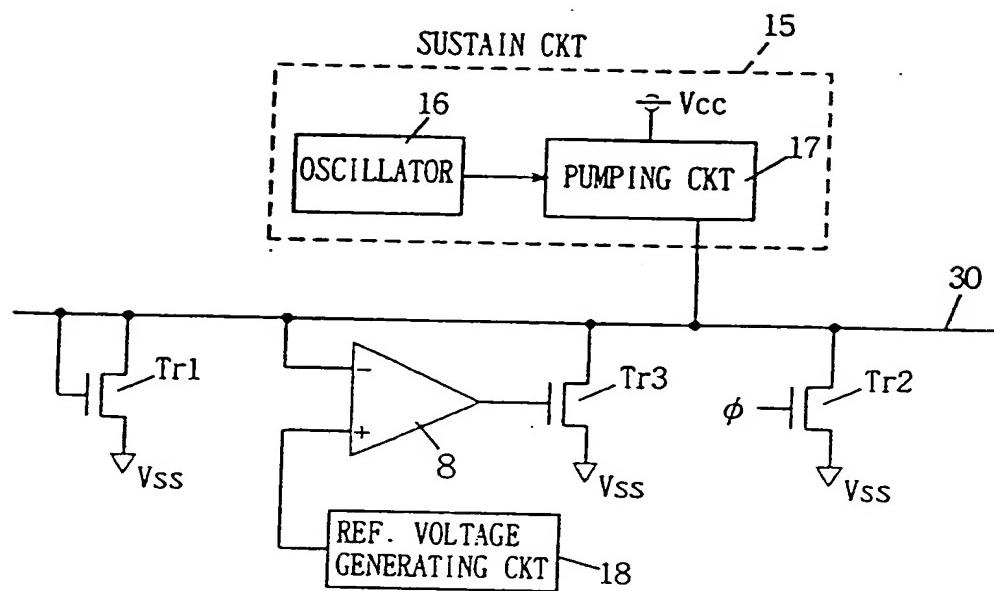
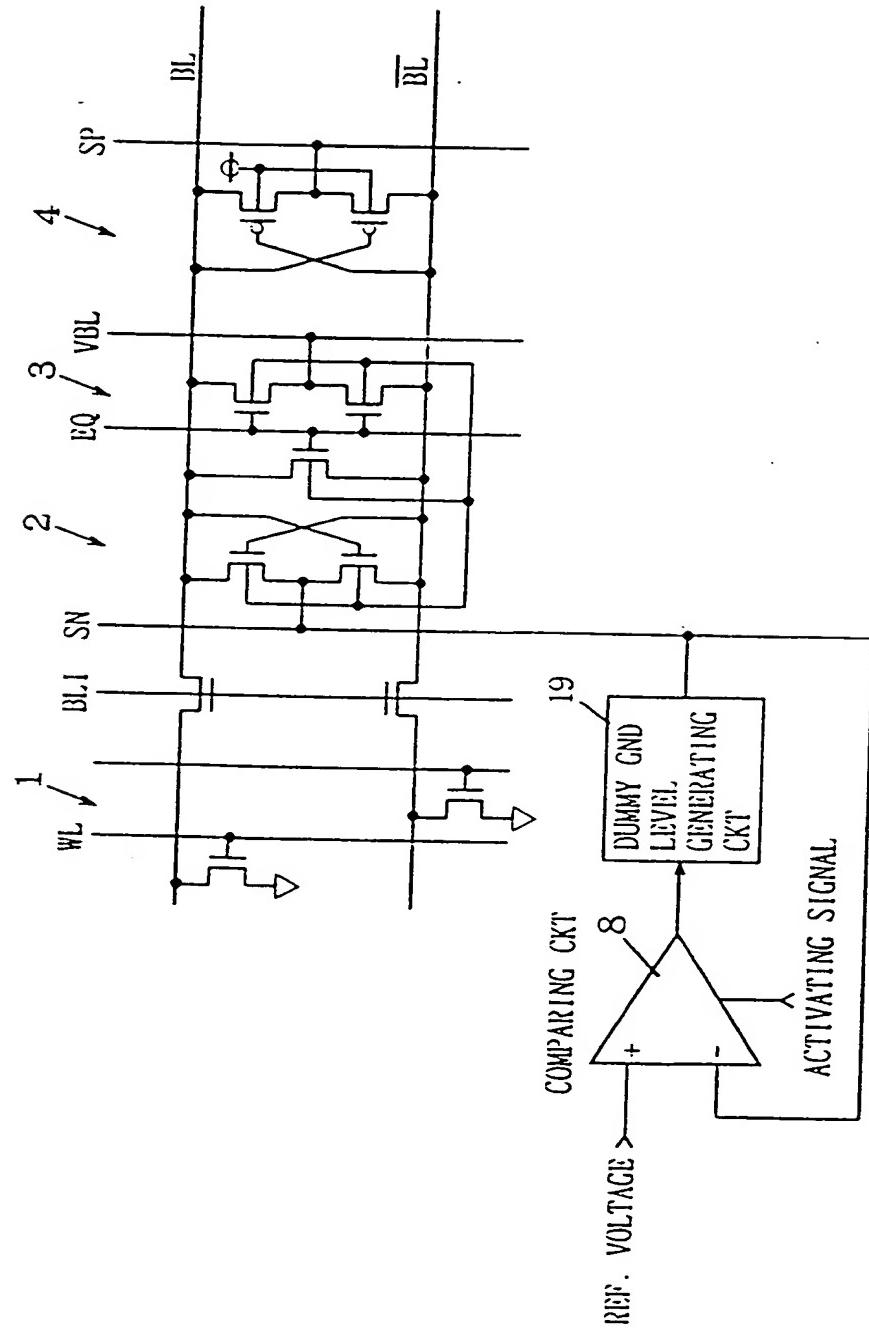


FIG. 23



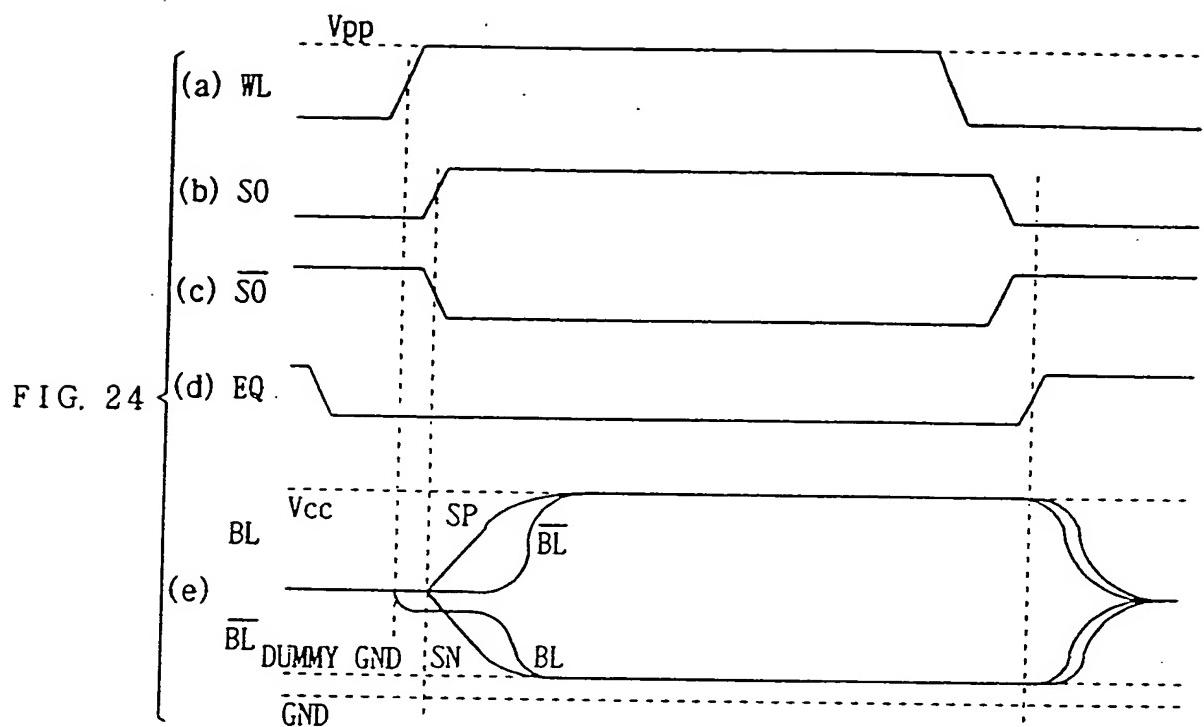


FIG. 25

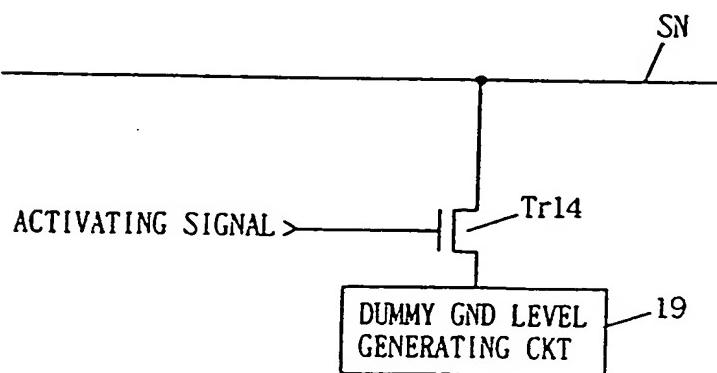


FIG. 26

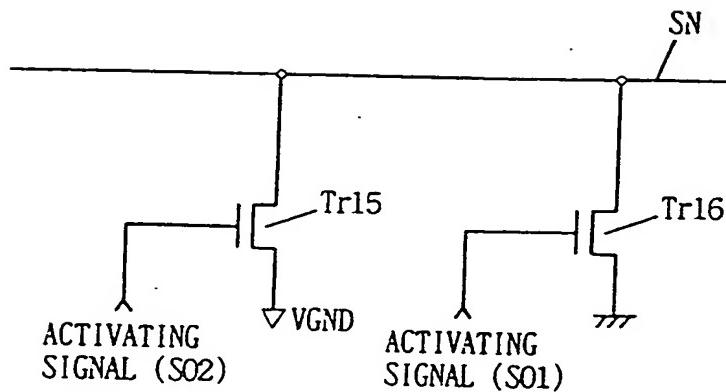
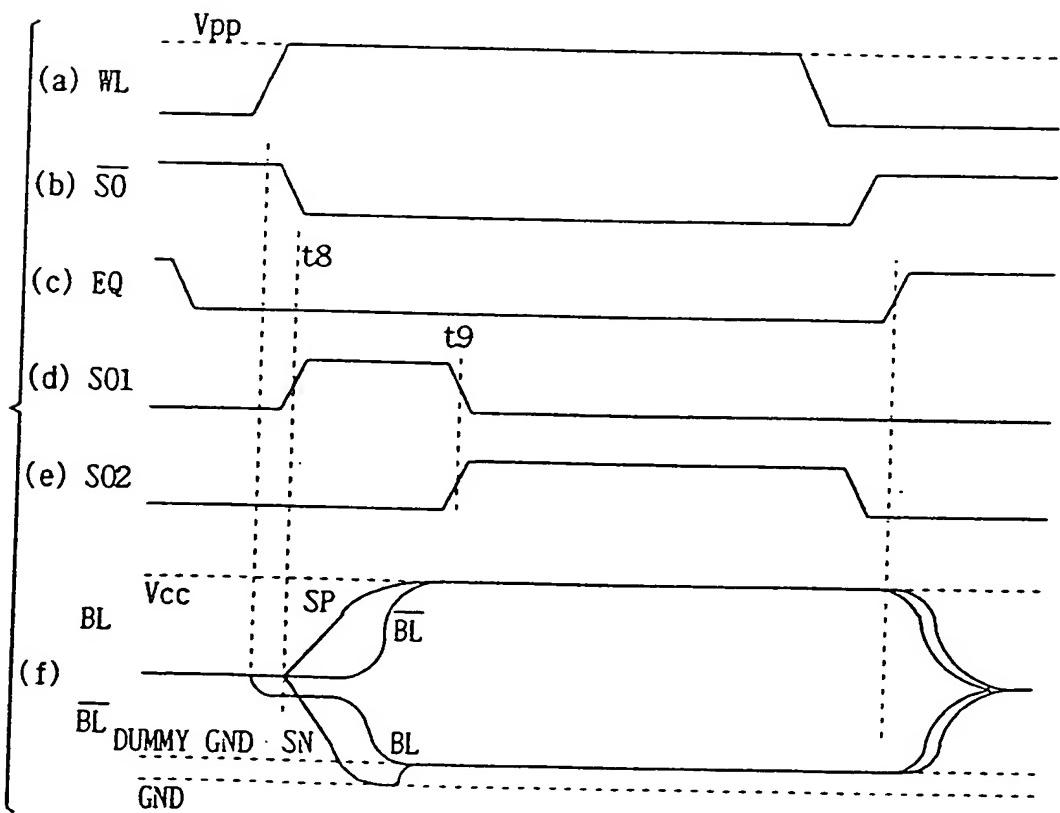
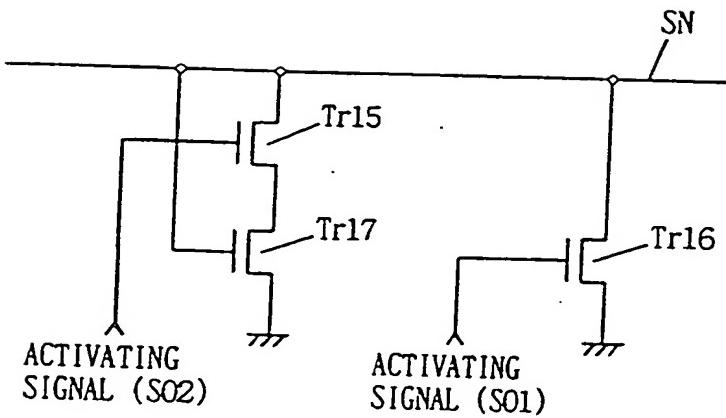


FIG. 27



F I G. 28



F I G. 29

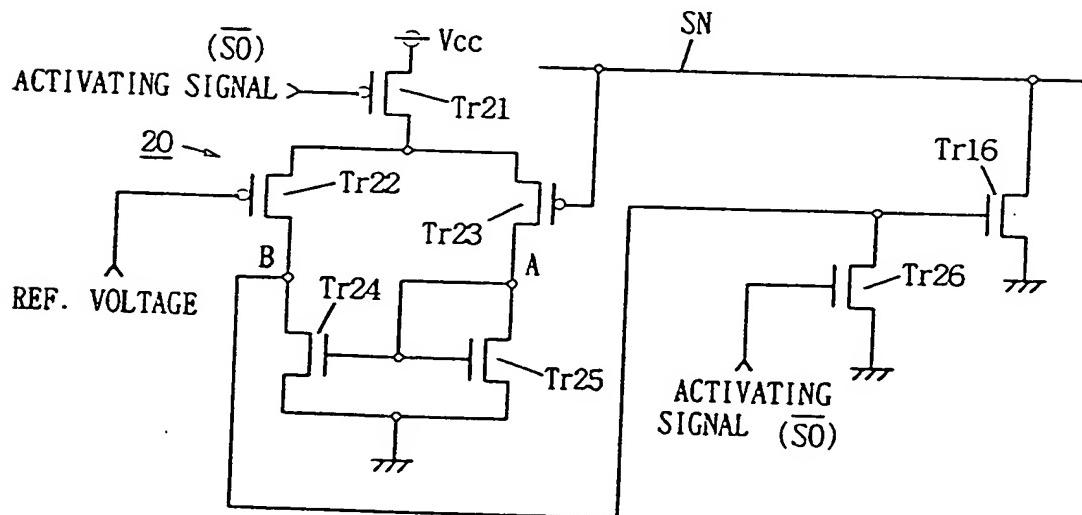


FIG. 30

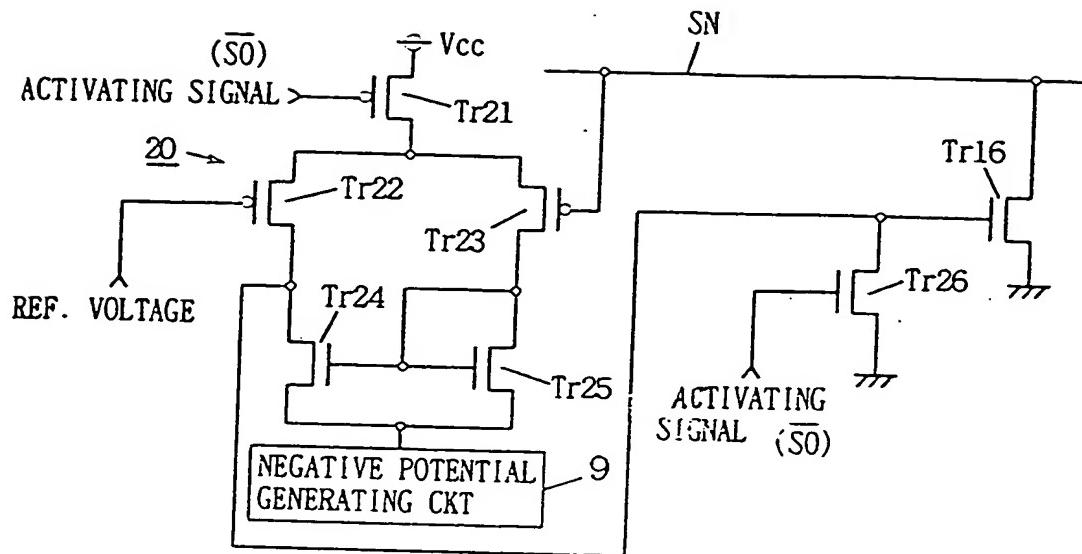


FIG. 31

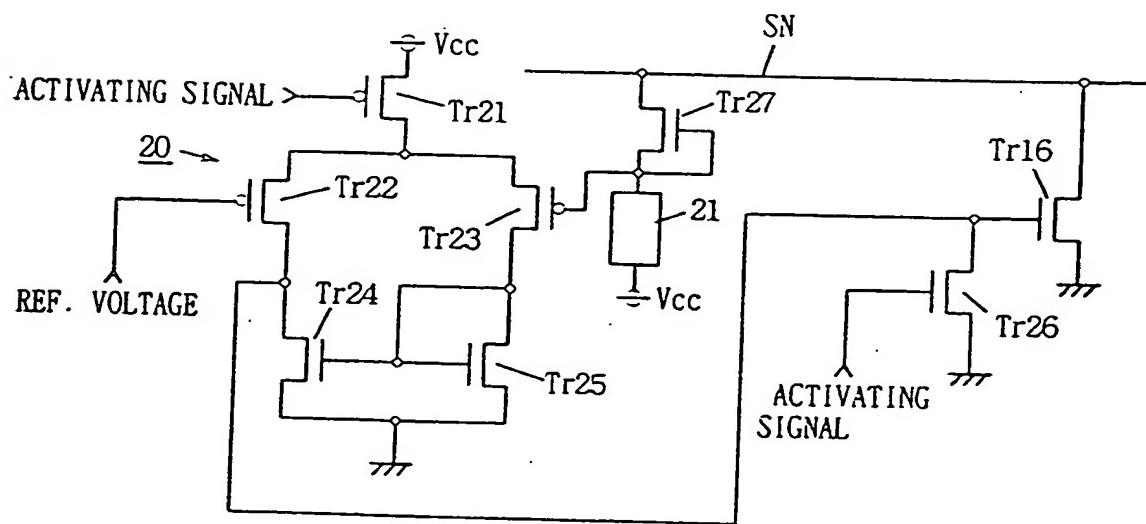


FIG. 32

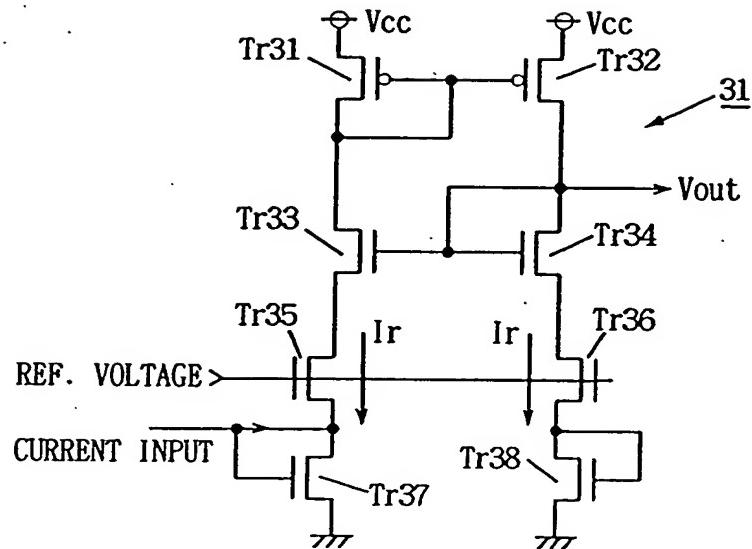
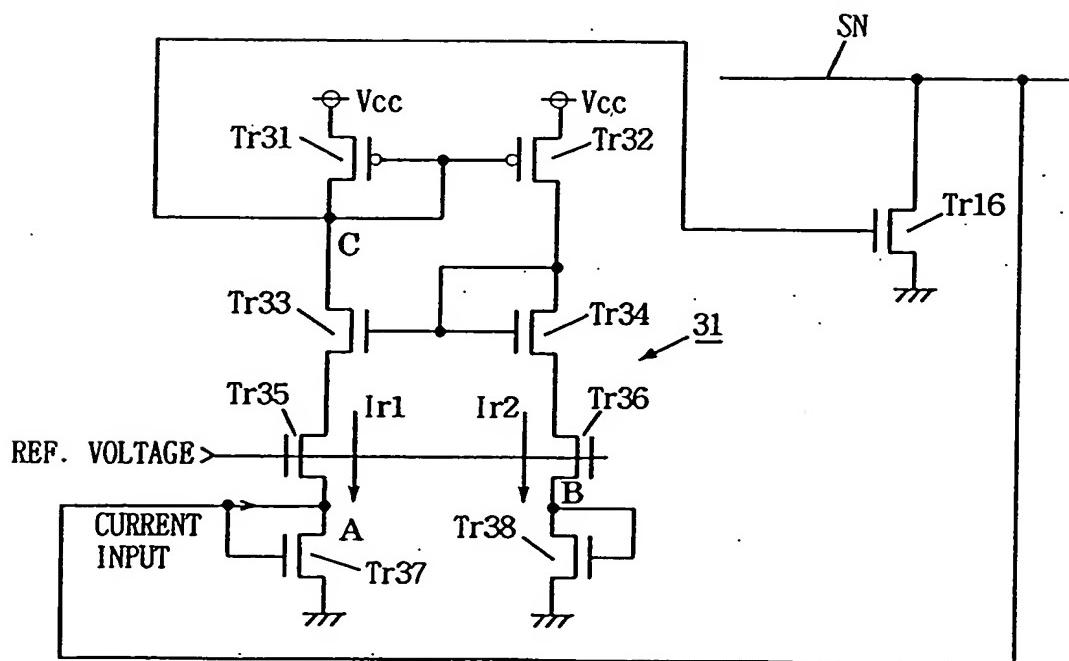
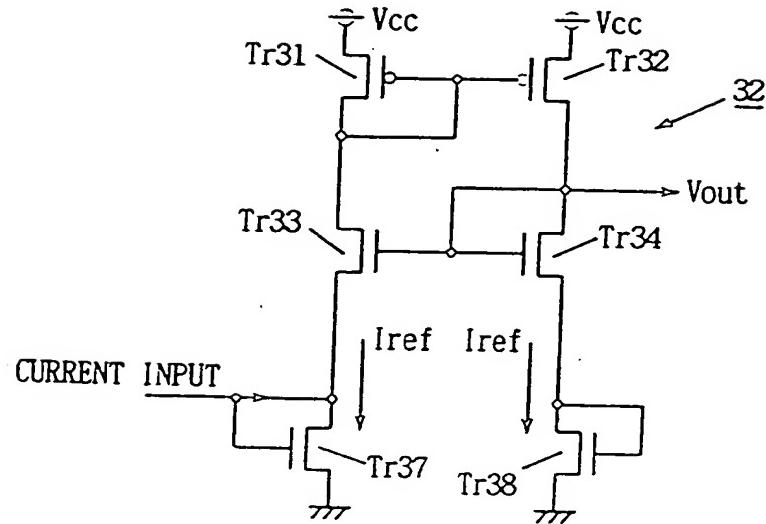


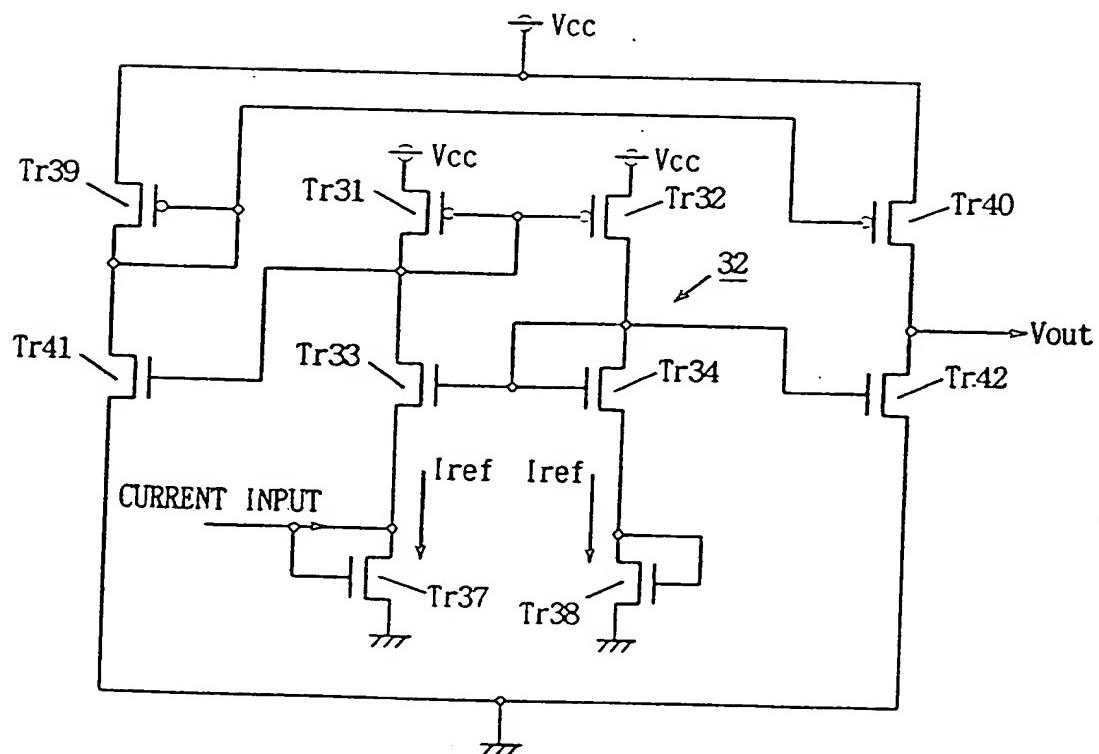
FIG. 33



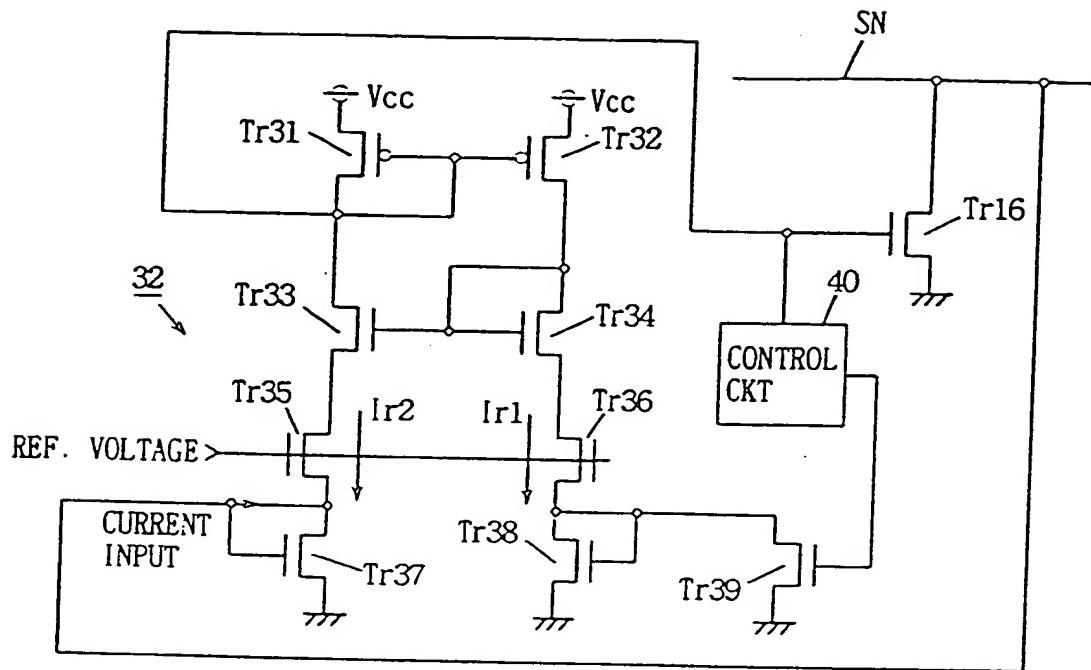
F I G. 34



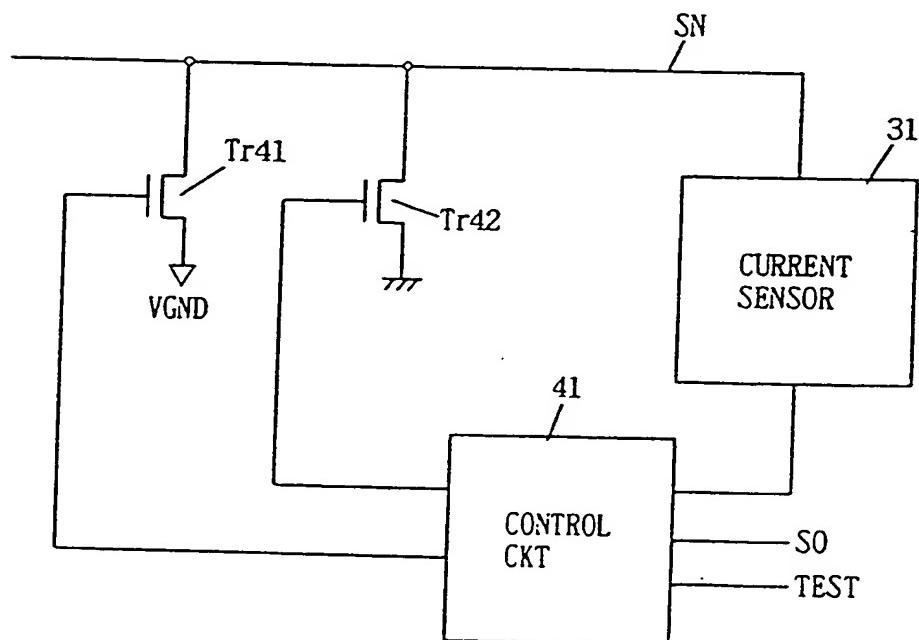
F I G. 35



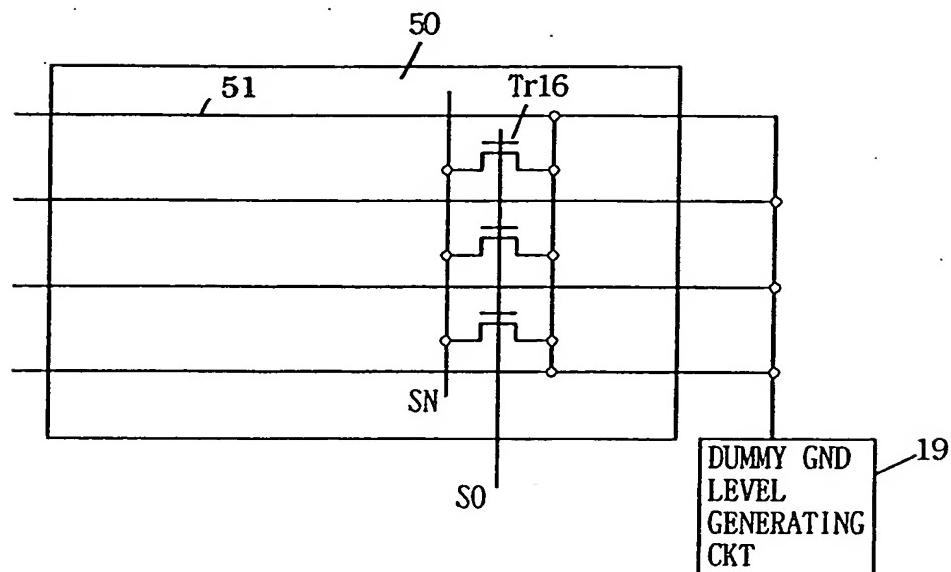
F I G. 36



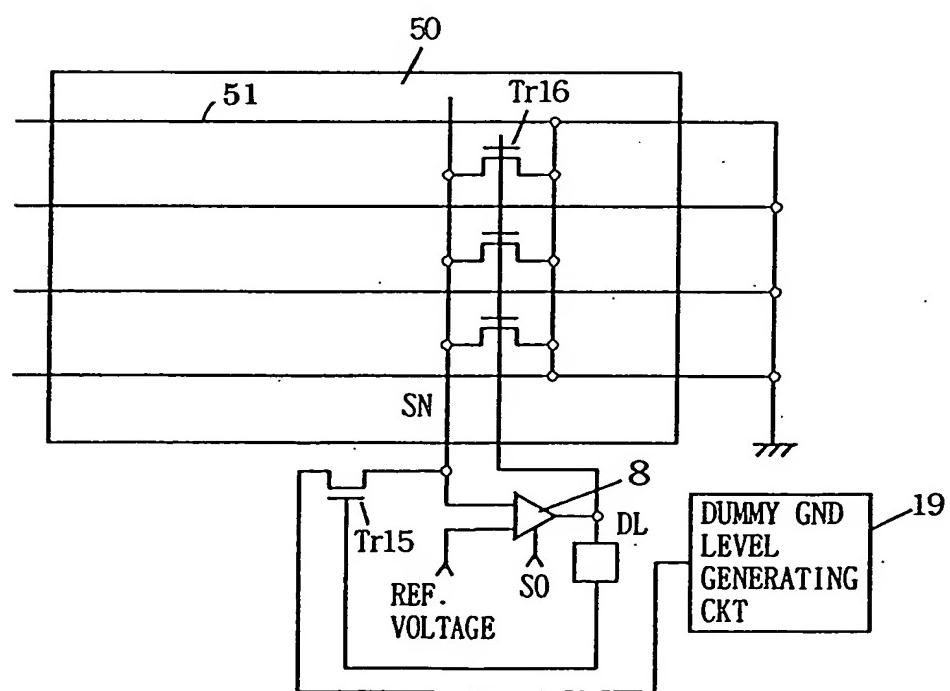
F I G. 37



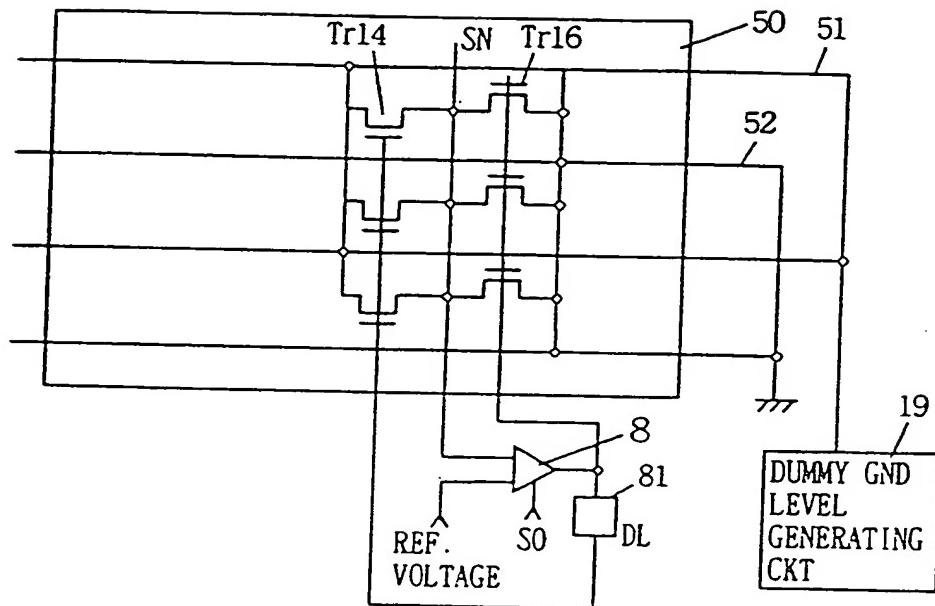
F I G. 38



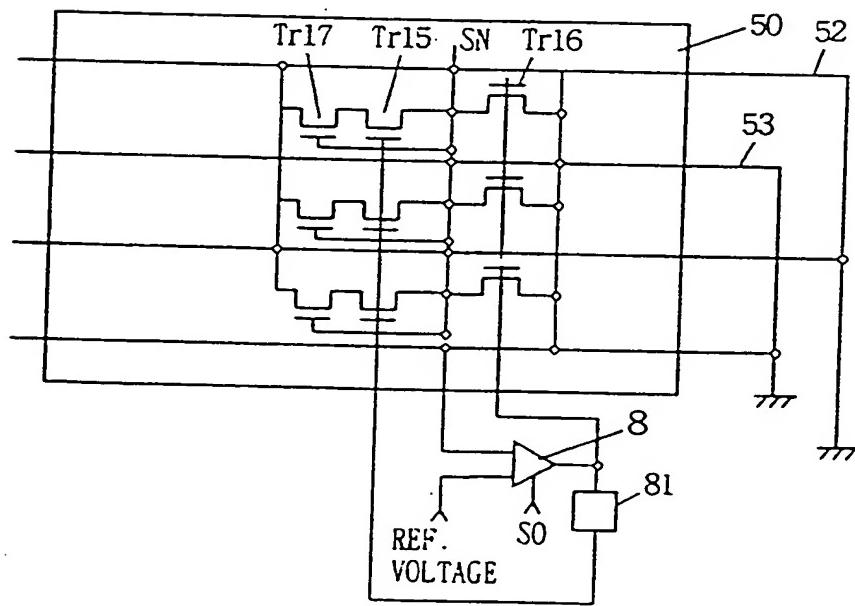
F I G. 39



F I G. 4 0



F I G. 4 1



F I G. 42

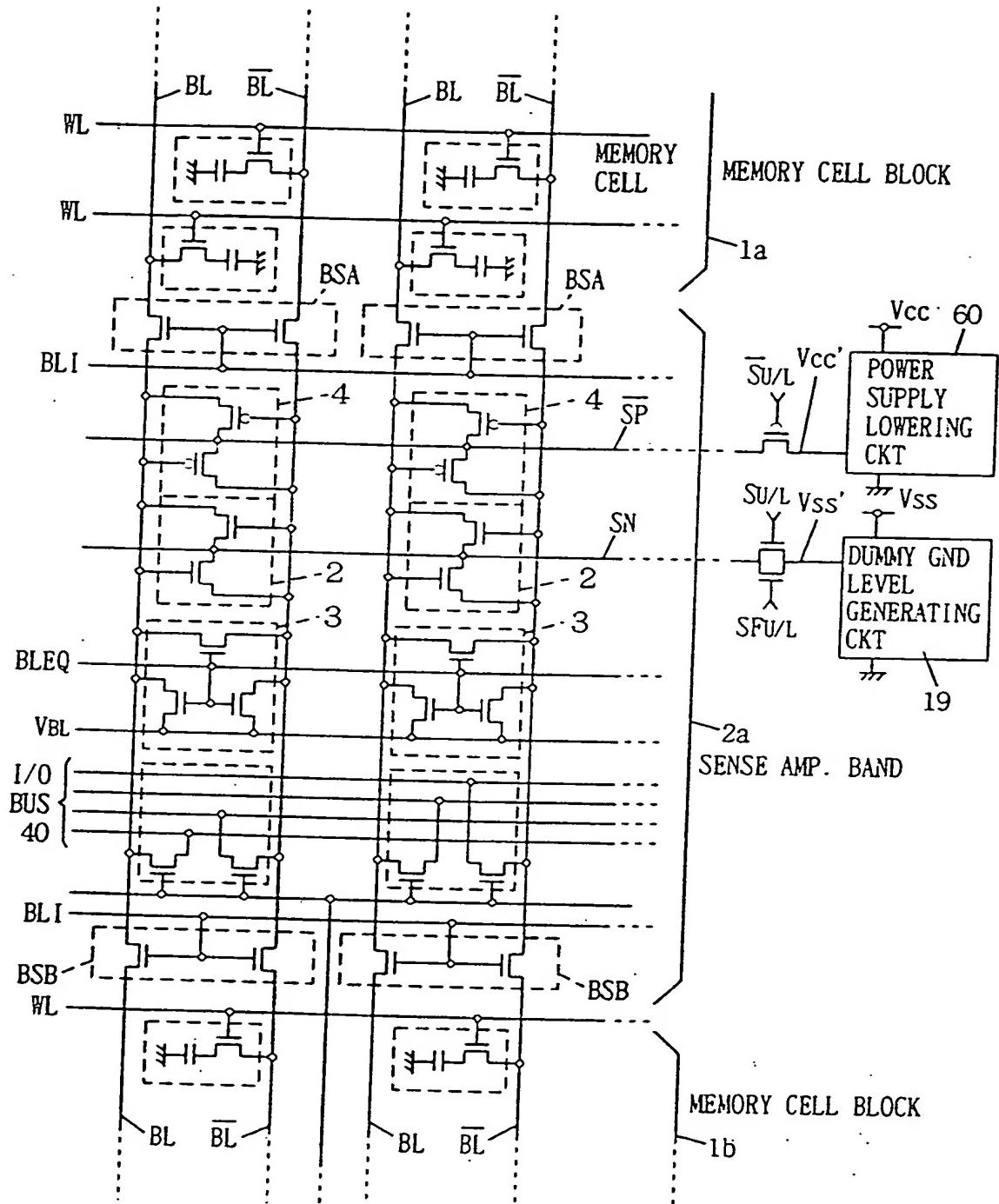
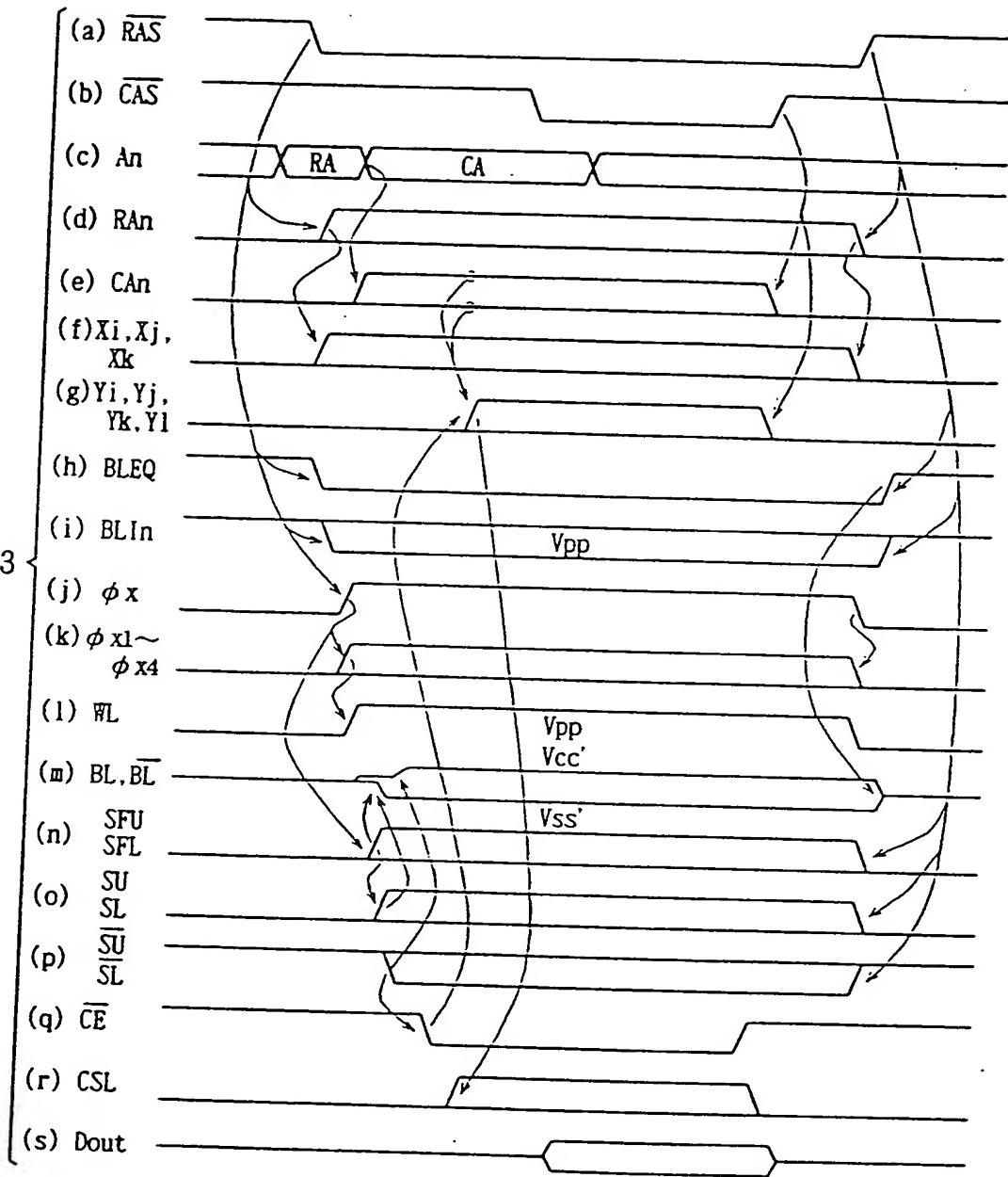
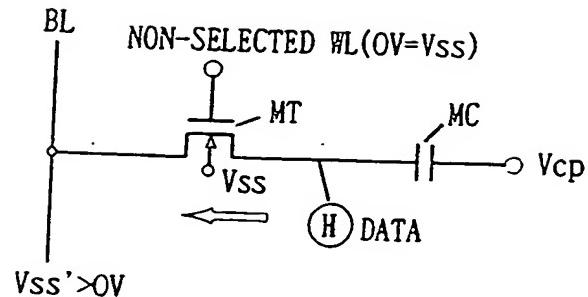


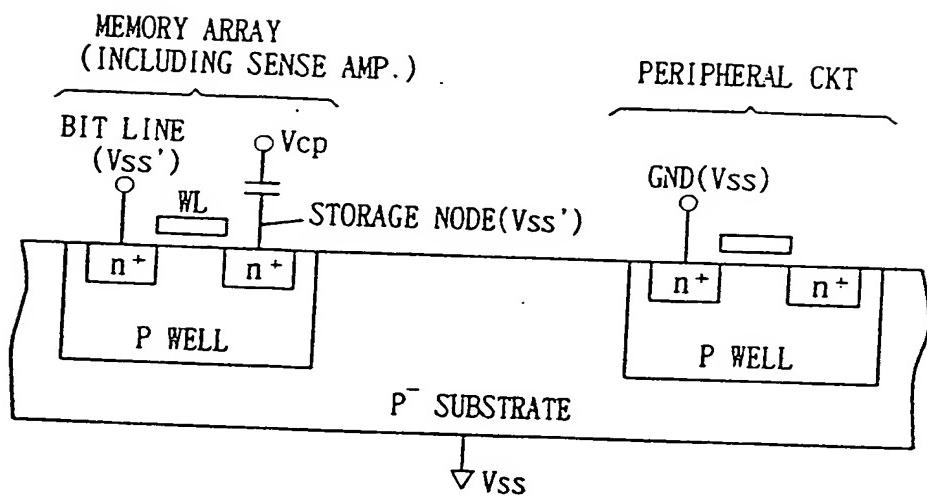
FIG. 43



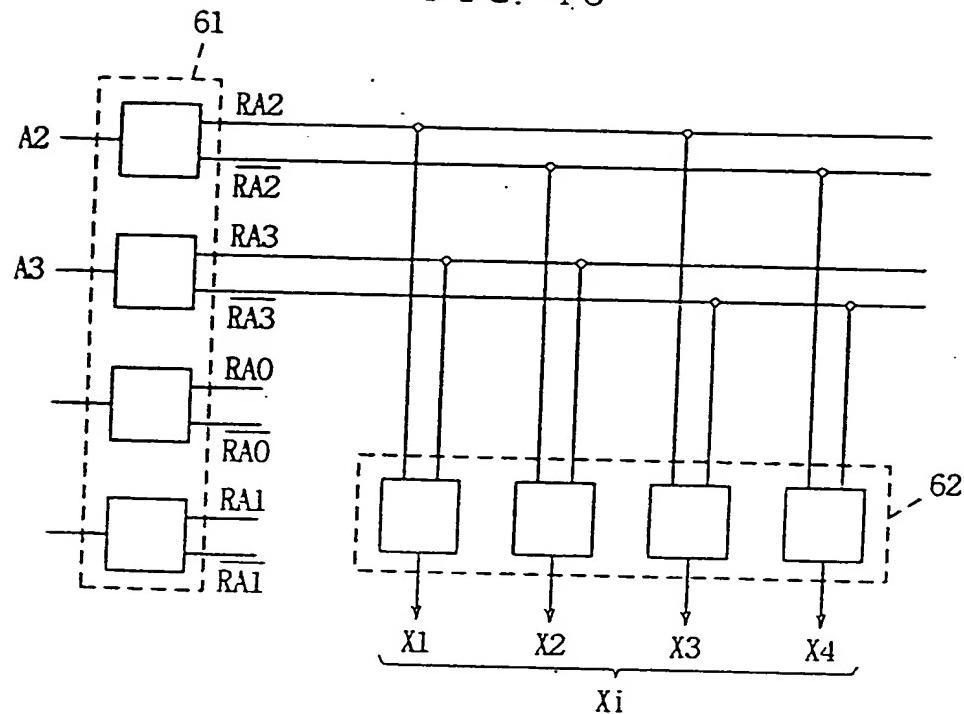
F I G. 44



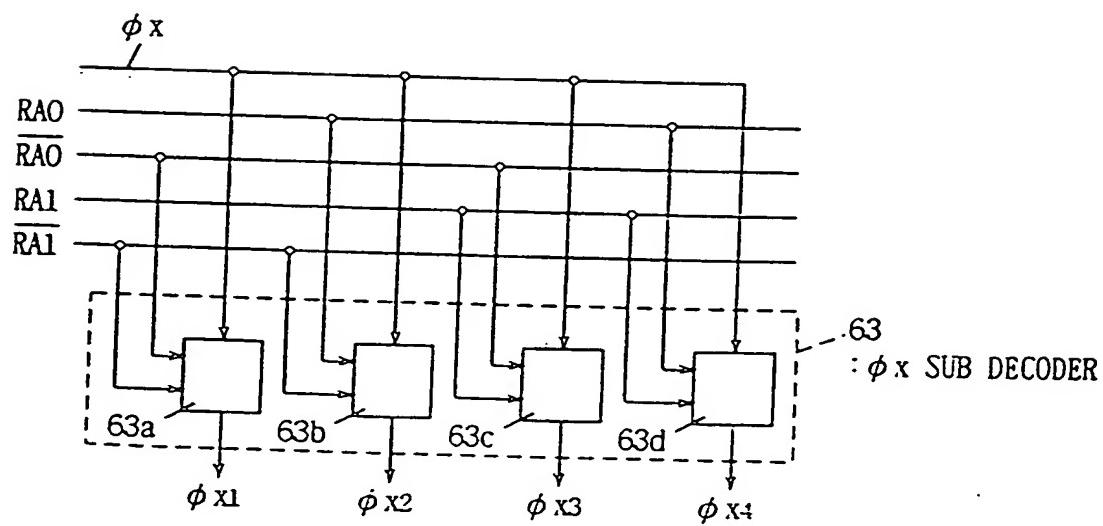
F I G. 45



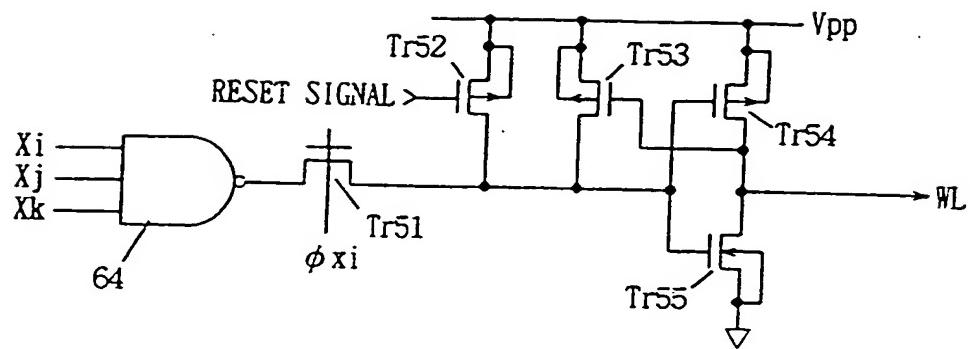
F I G. 4 6



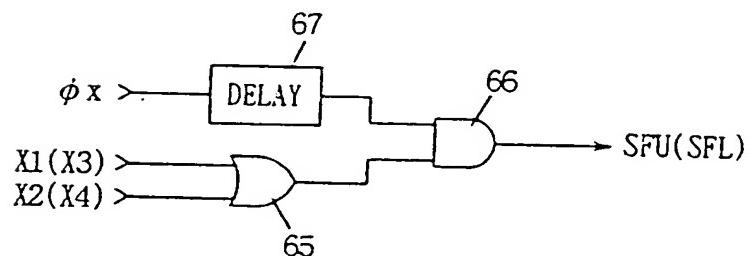
F I G. 4 7



F I G. 48



F I G. 49



F I G. 50

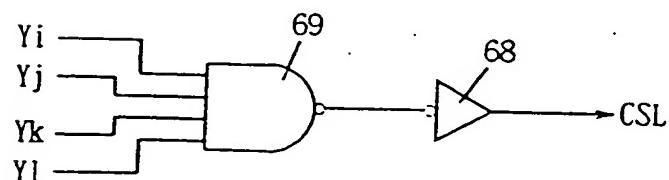
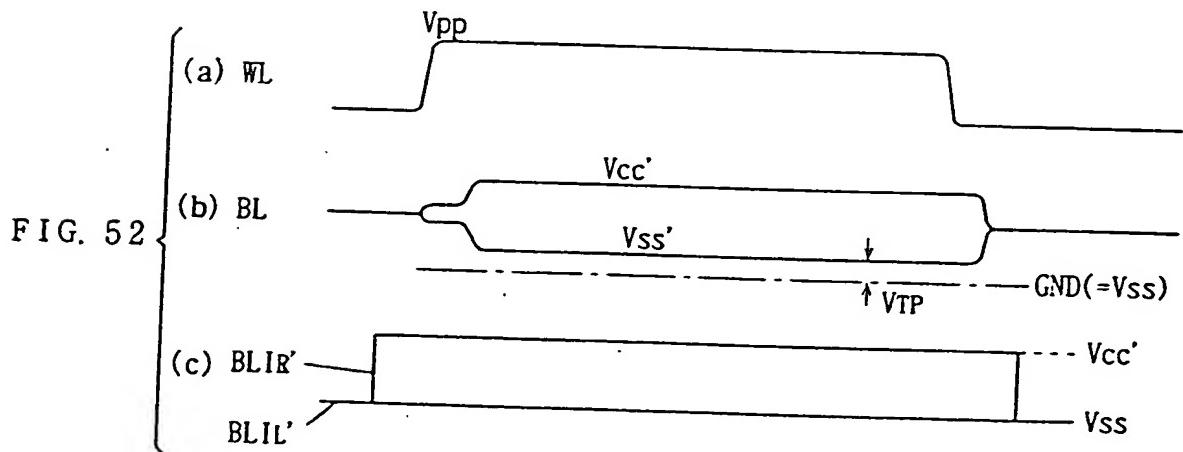
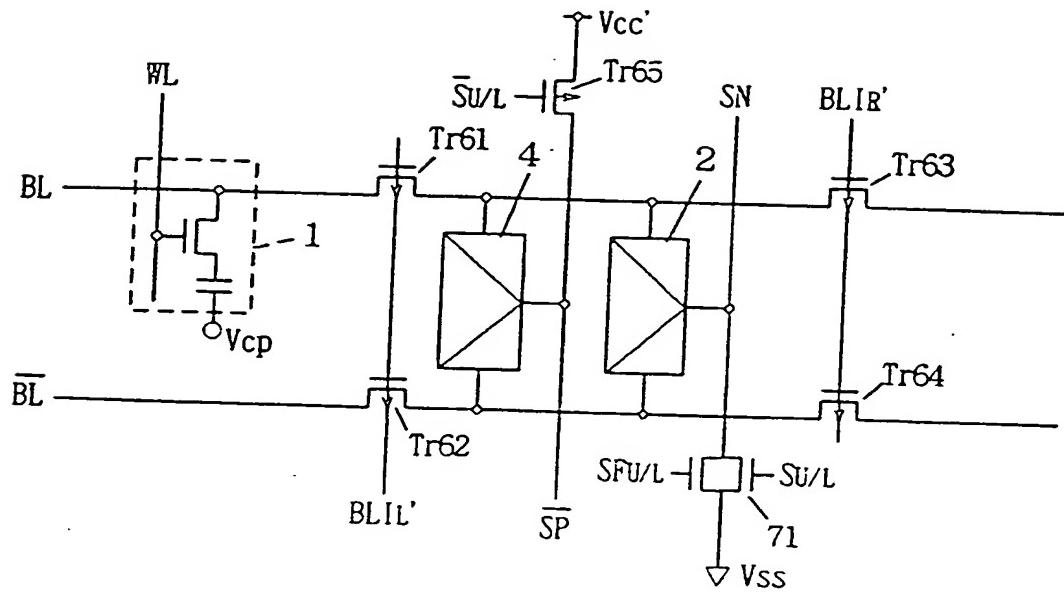


FIG. 51



F I G. 53

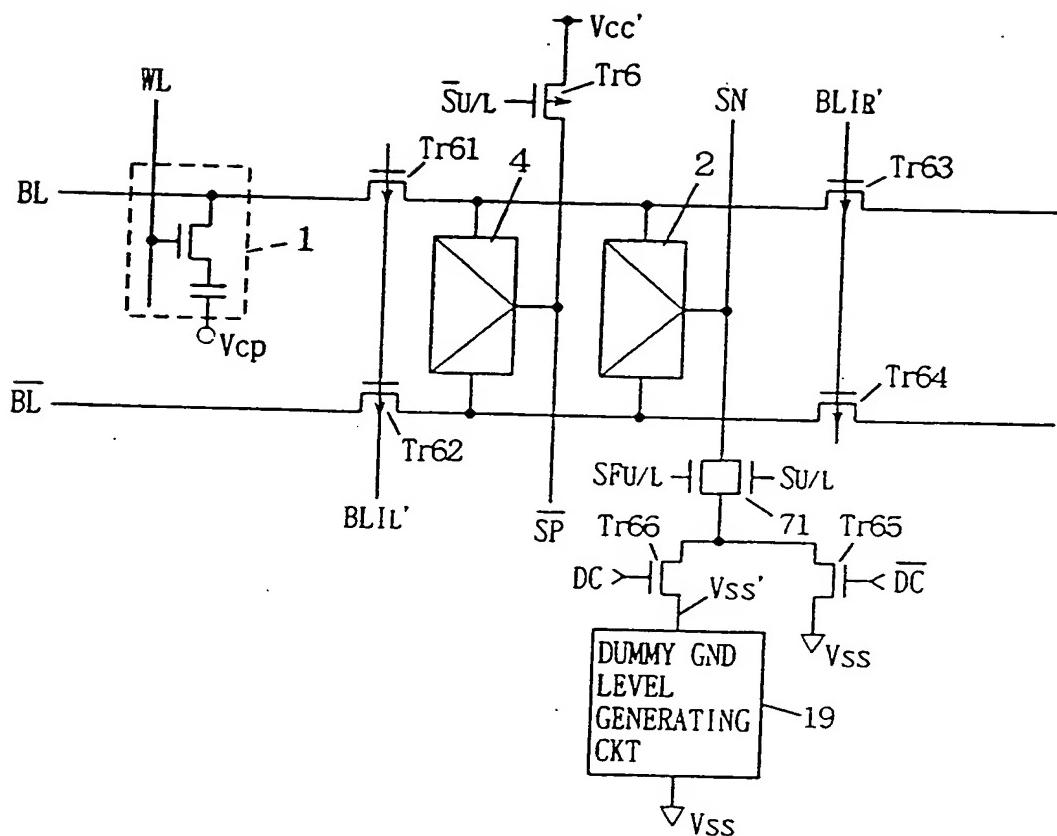
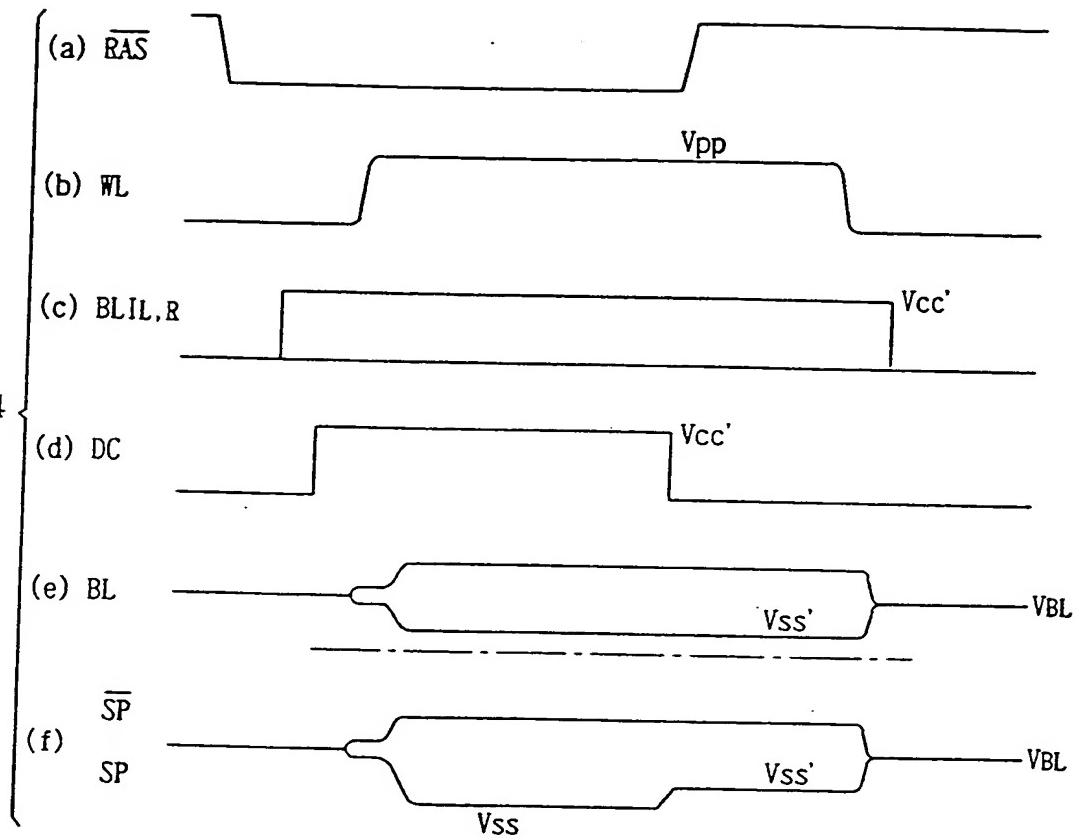
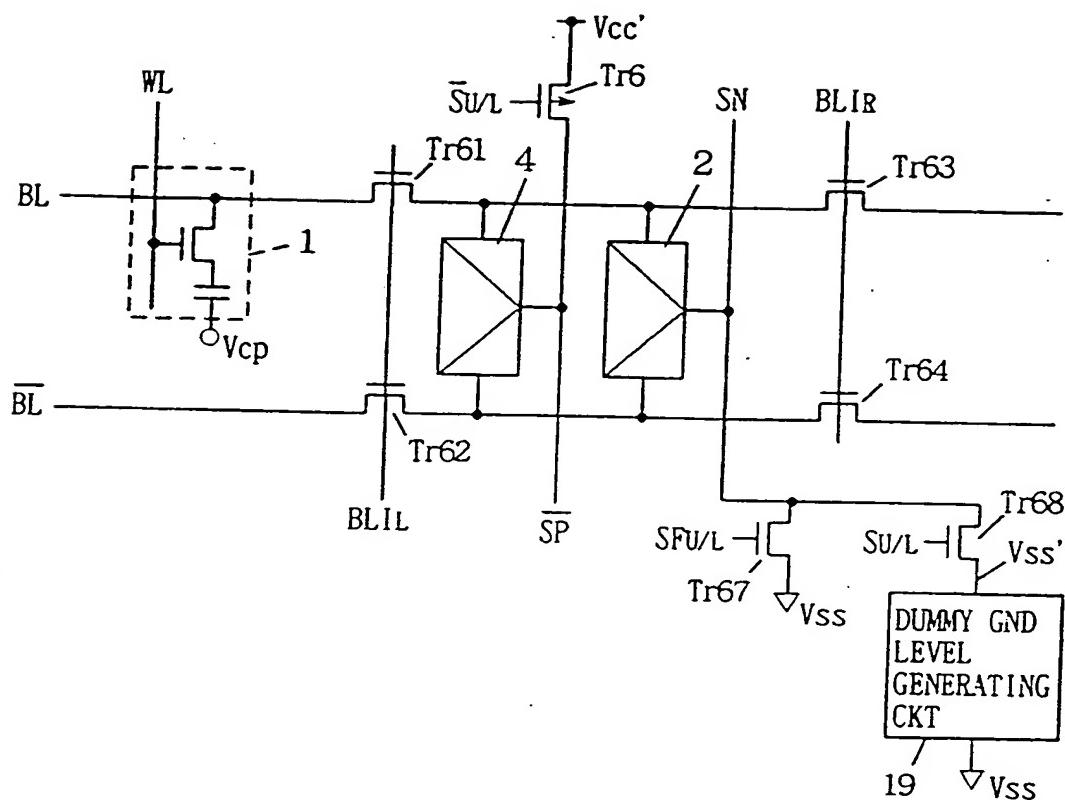


FIG. 54



F I G. 55



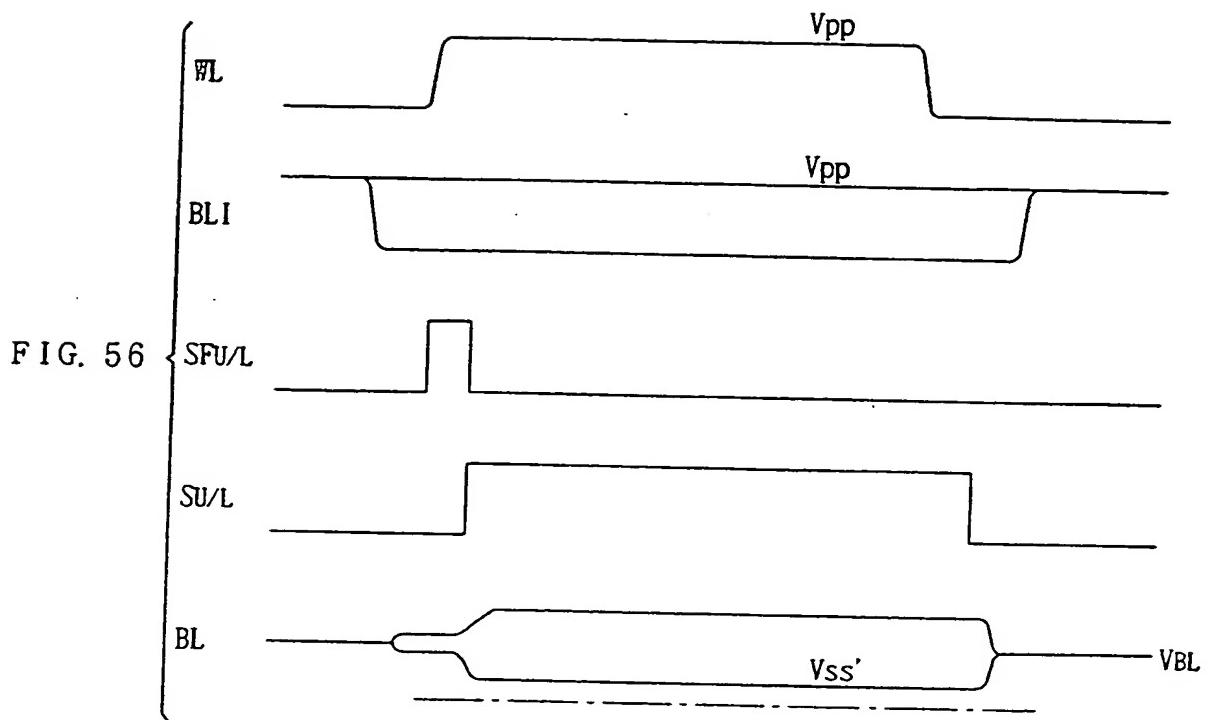


FIG. 57

EXT. V_{CC} —————

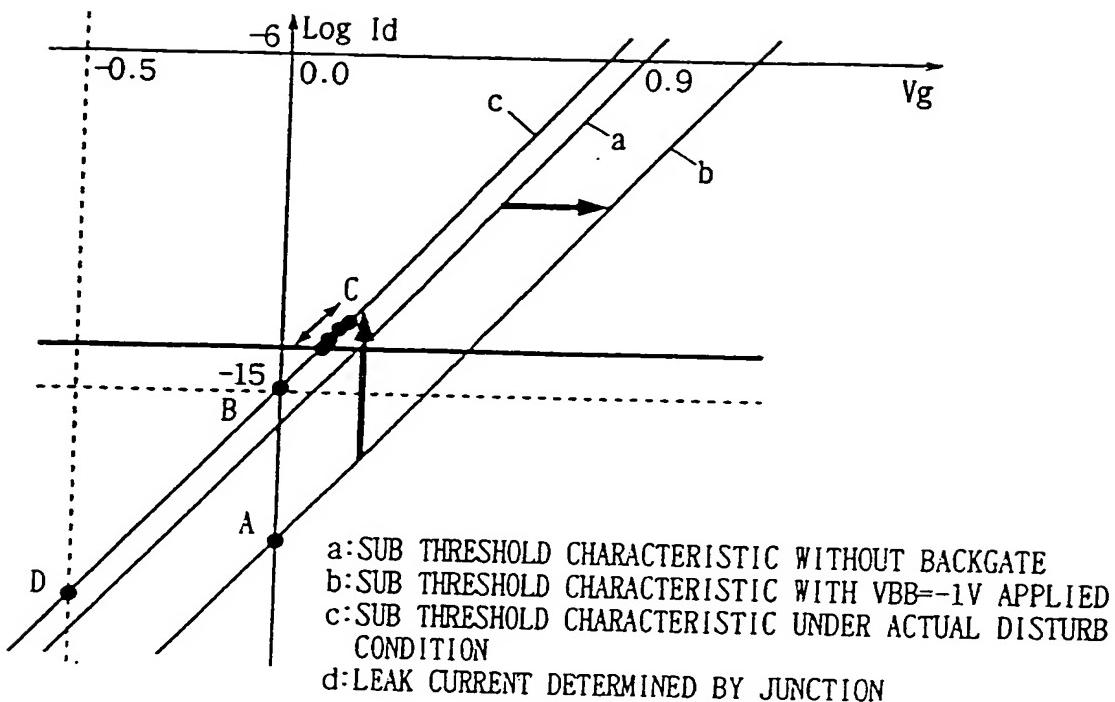
INT. Vcc ——————

IC OPERATIONAL VOLTAGE

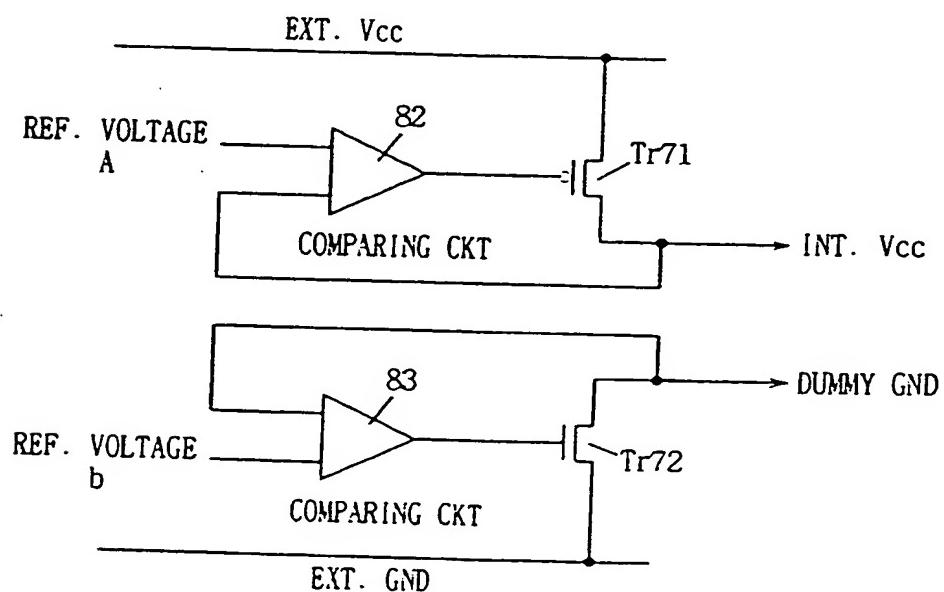
DUMMY GND _____ ↓

EXT. GND _____

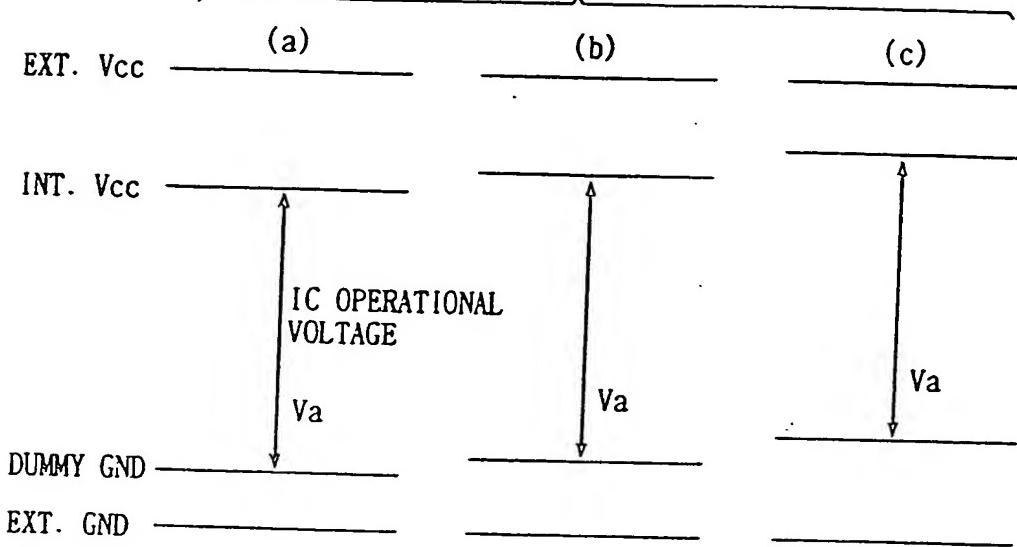
F I G. 58



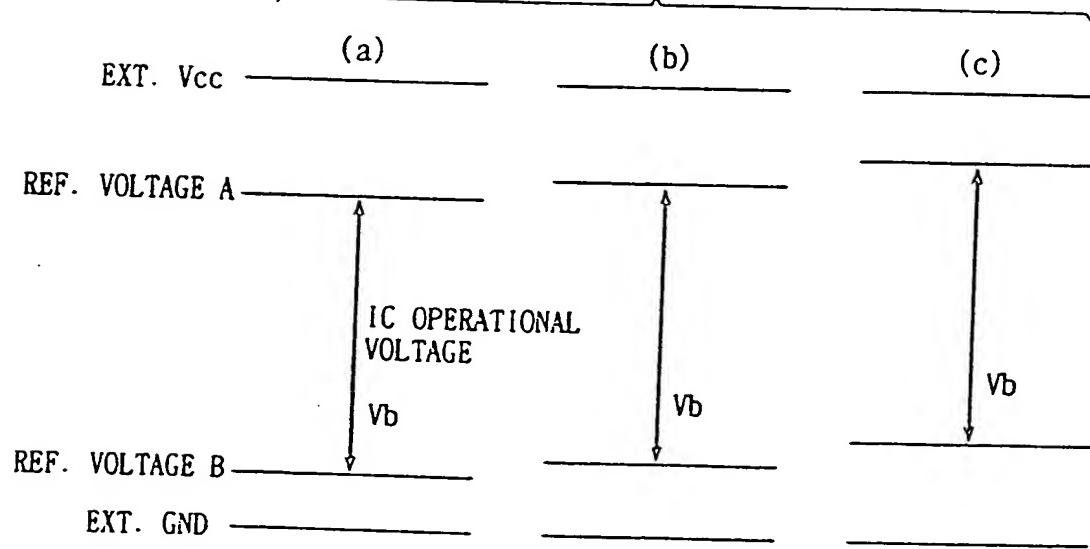
F I G. 59



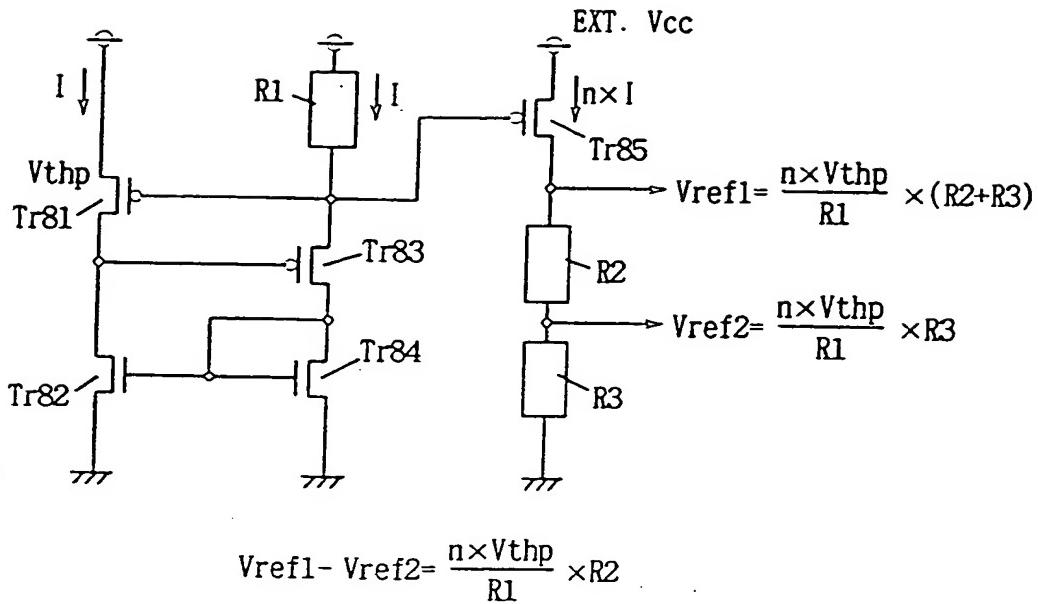
F I G. 6 0



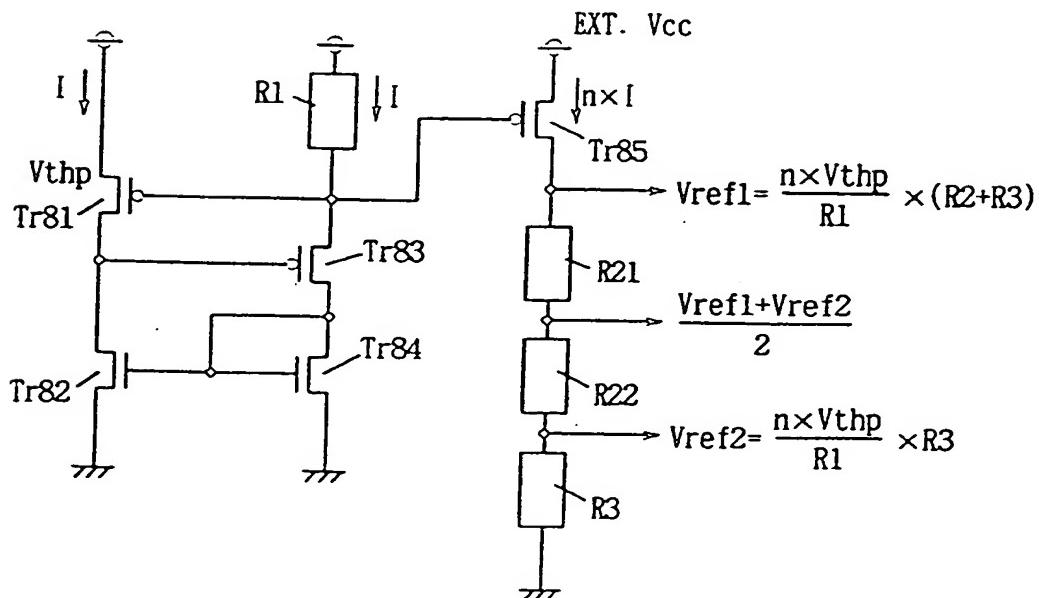
F I G. 6 1



F I G. 62



F I G. 63



F I G. 64

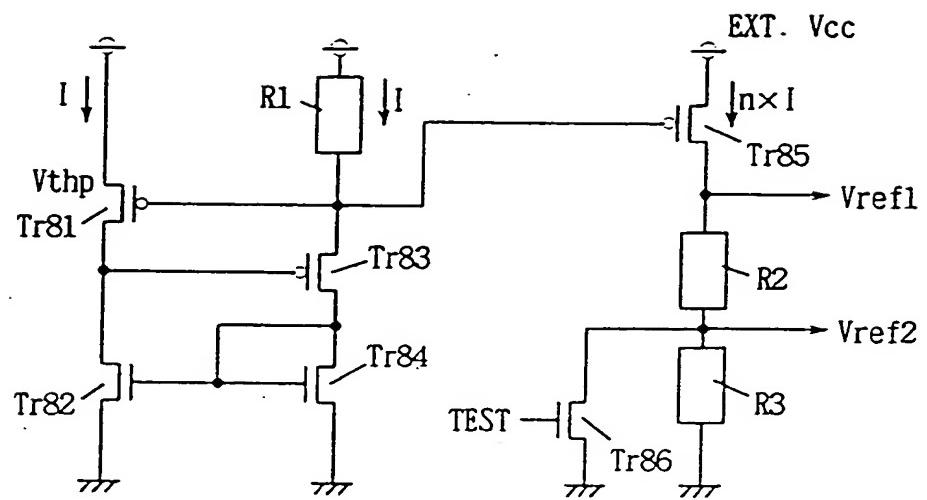


FIG. 65

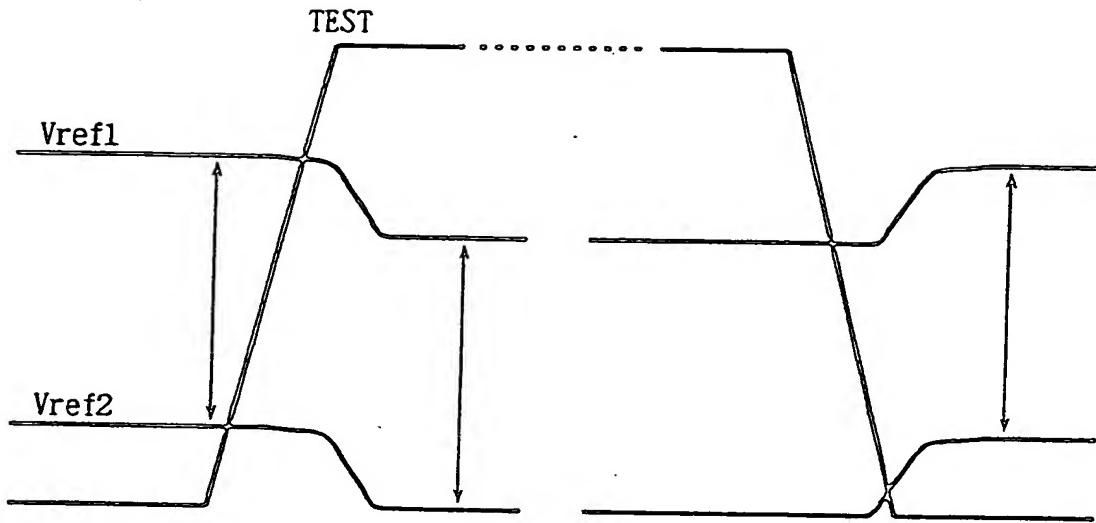
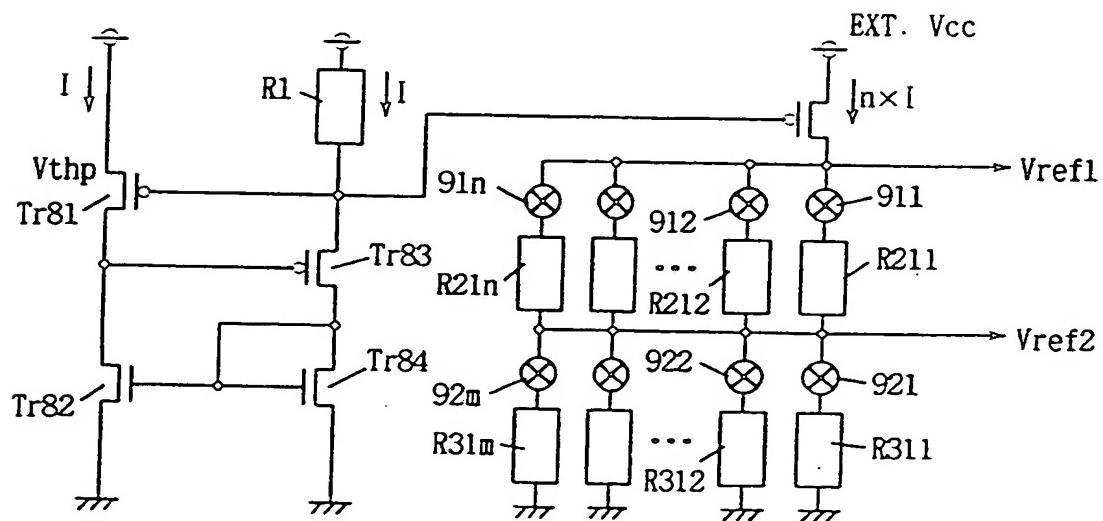
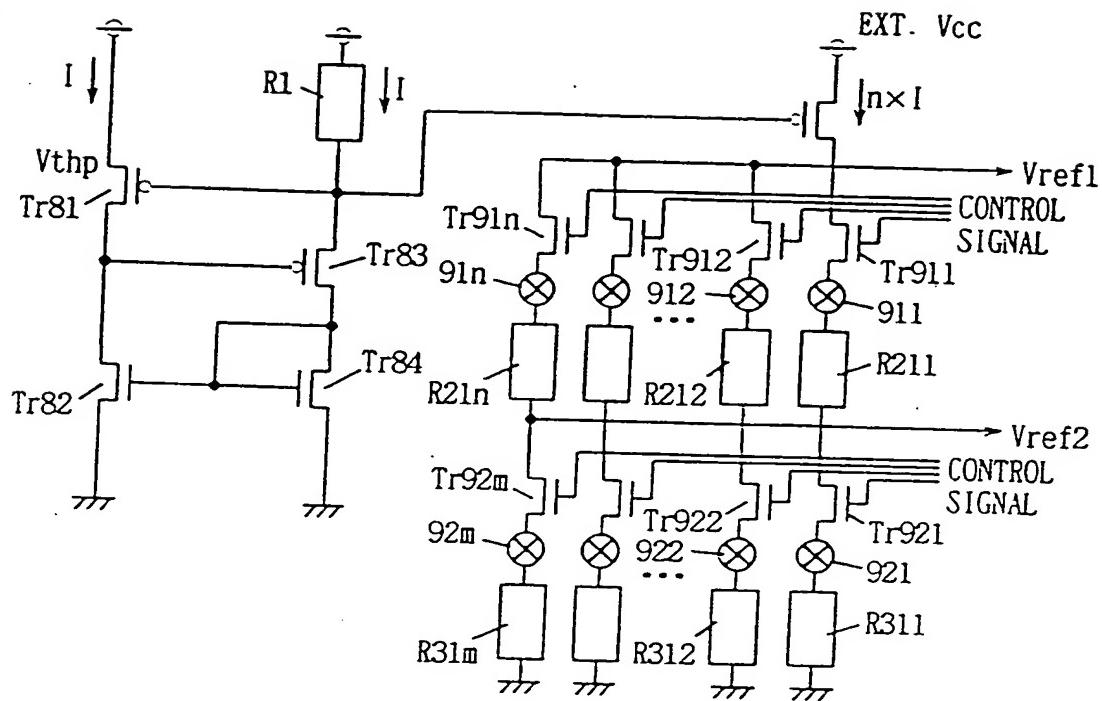


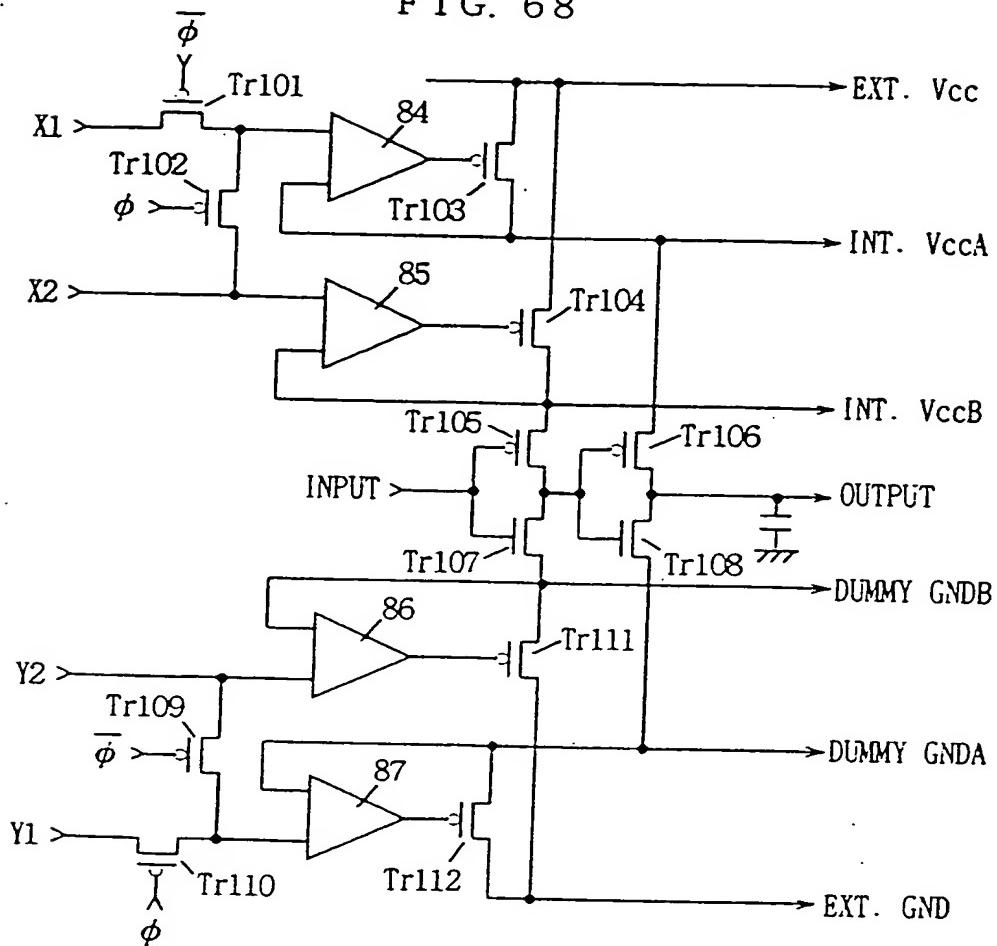
FIG. 66



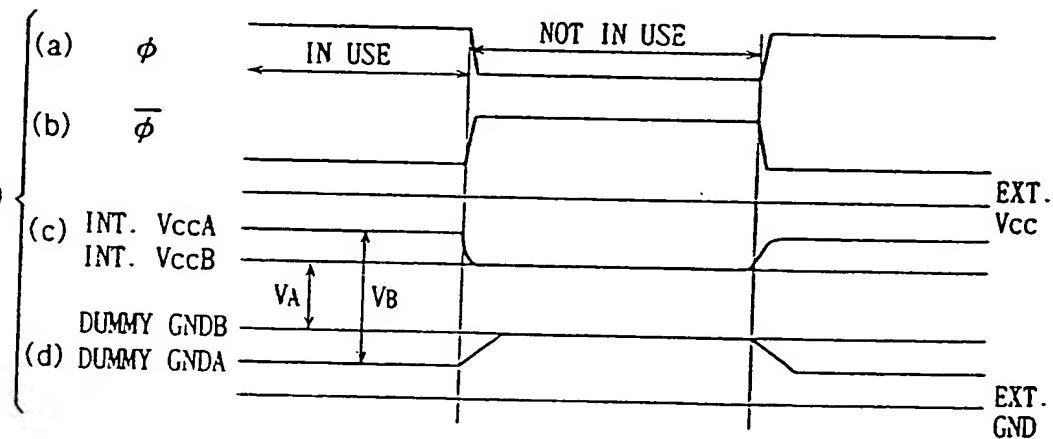
F I G. 67

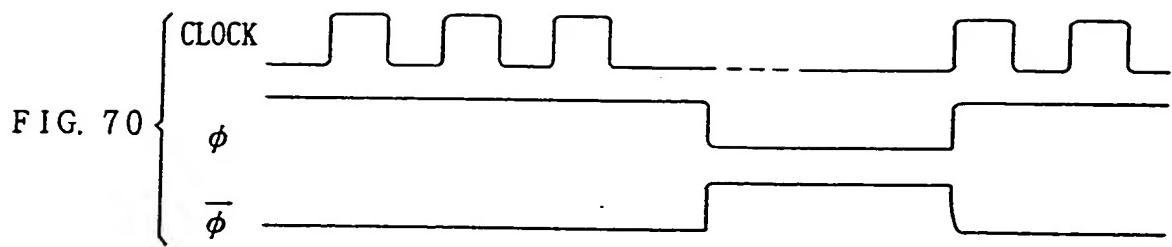


F I G. 68

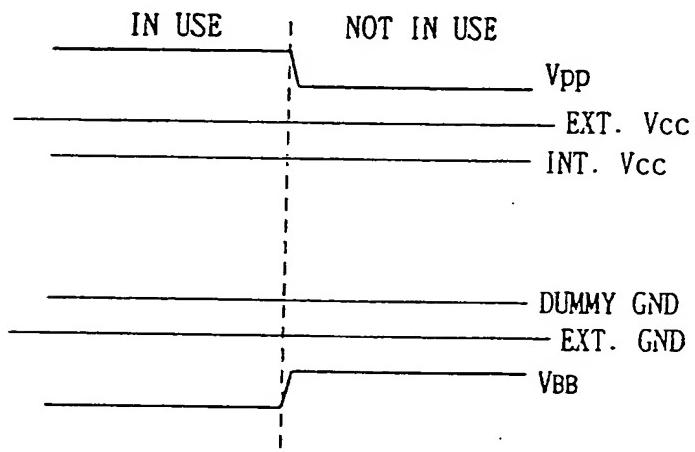


F I G. 69

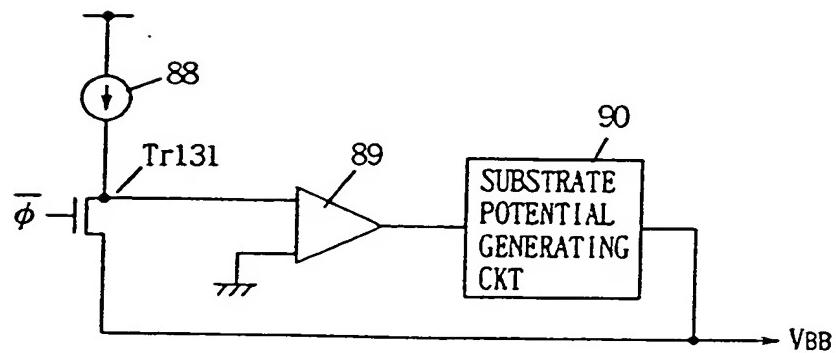




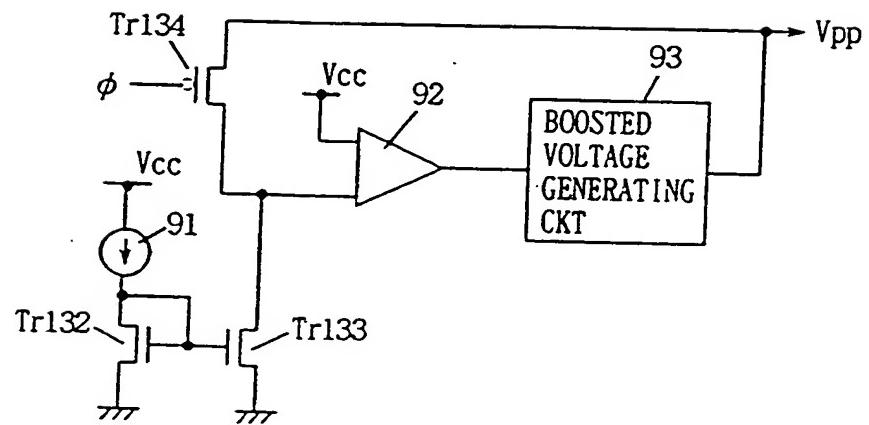
F I G. 71



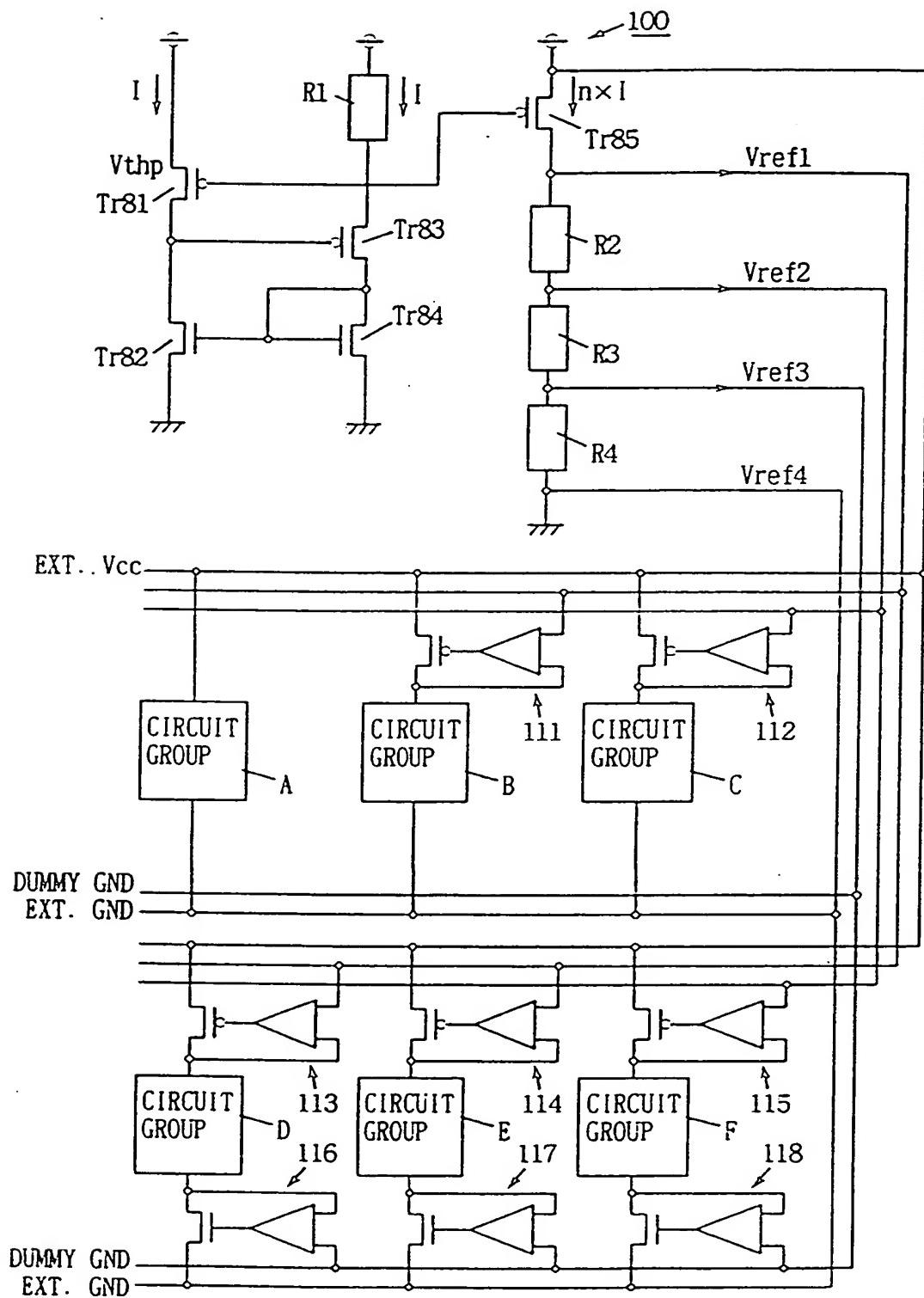
F I G. 72



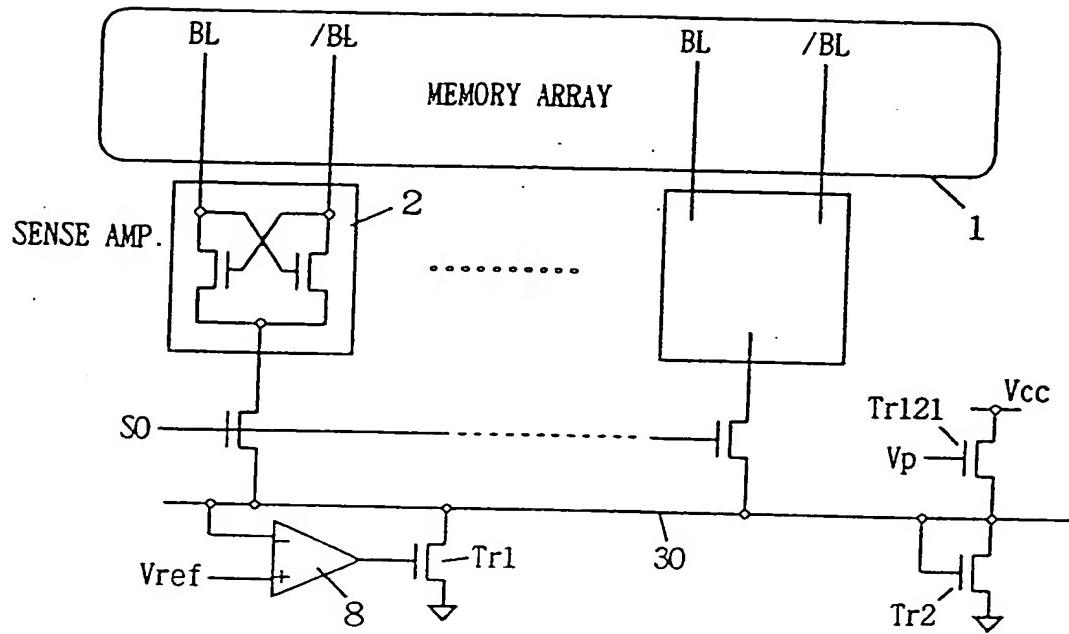
F I G. 73



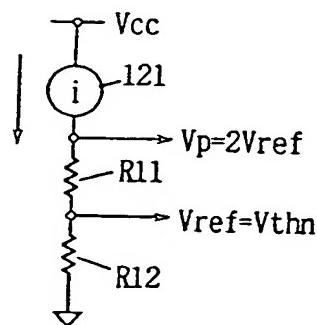
F I G. 74



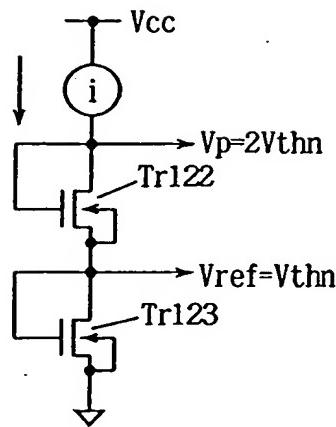
F I G. 75



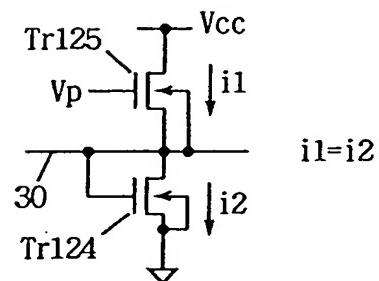
F I G. 76



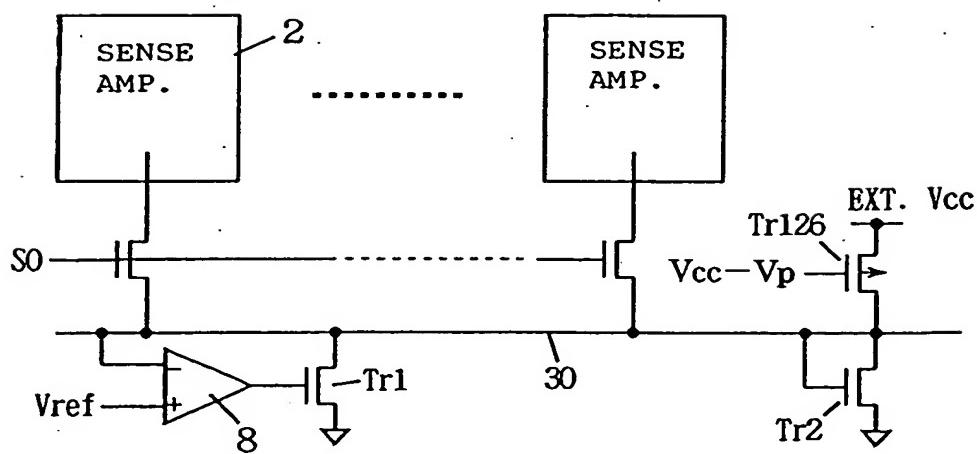
F. I G. 77



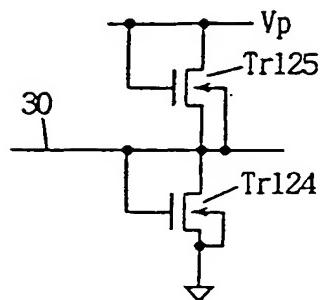
F I G. 78



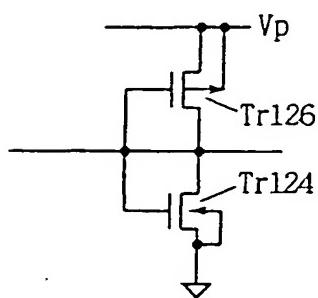
F I G. 79



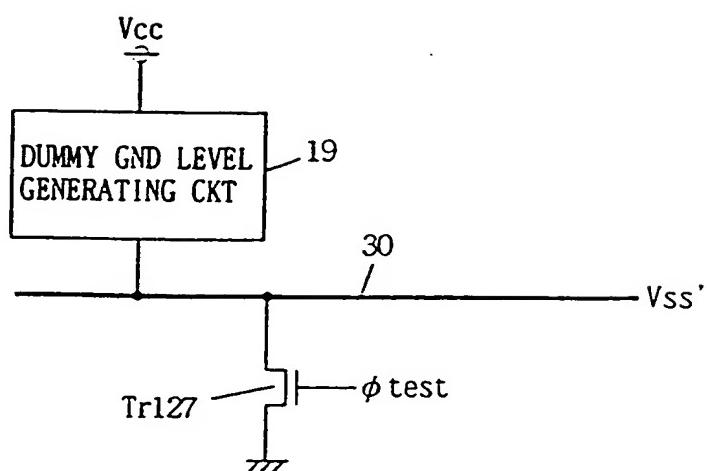
F I G. 80

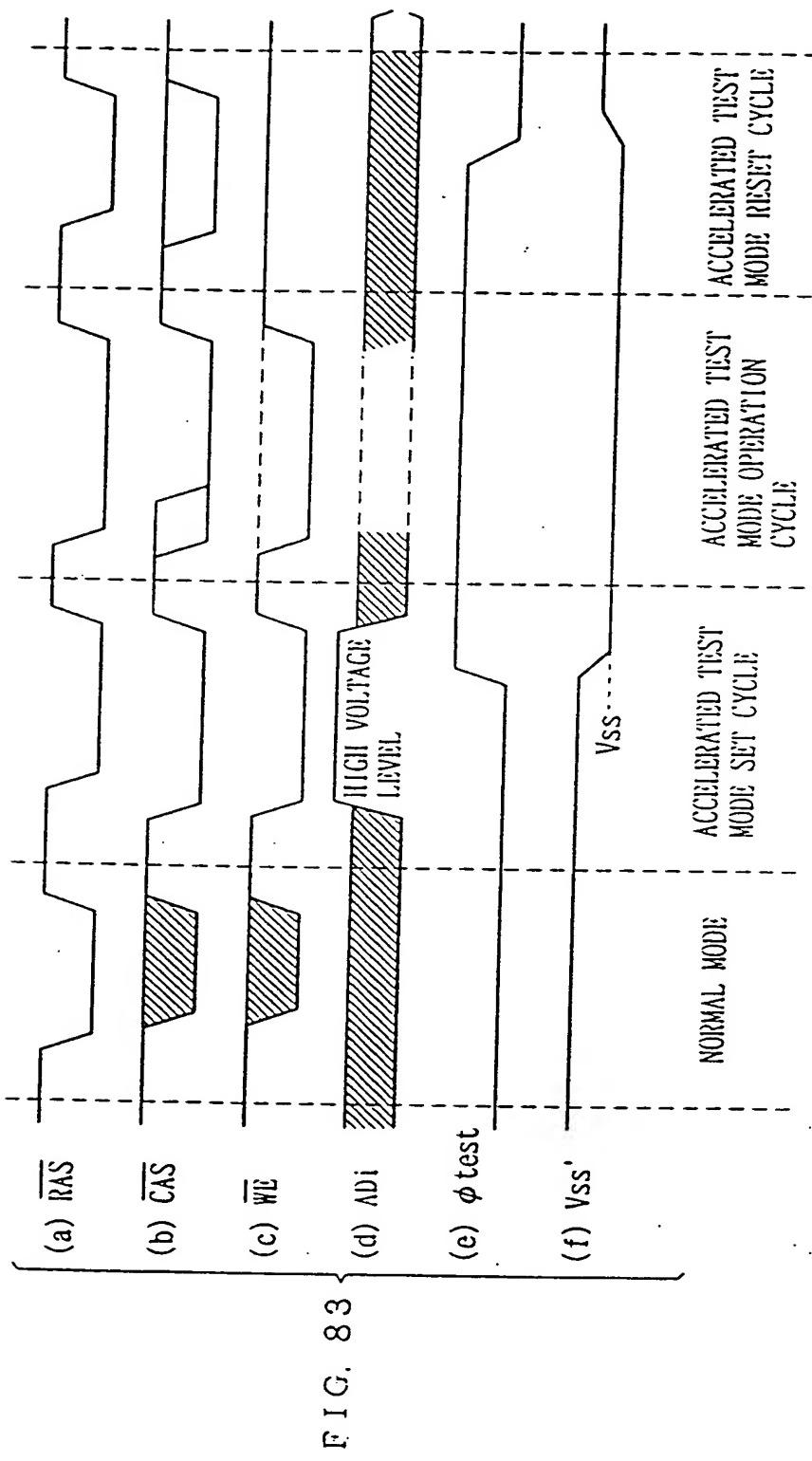


F I G. 81

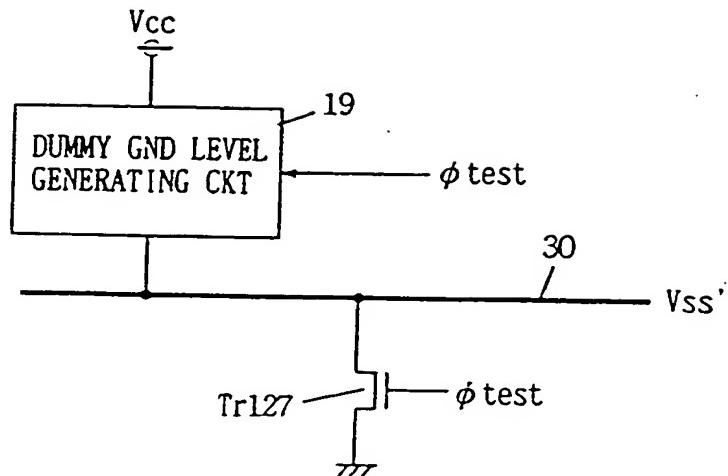


F I G. 82

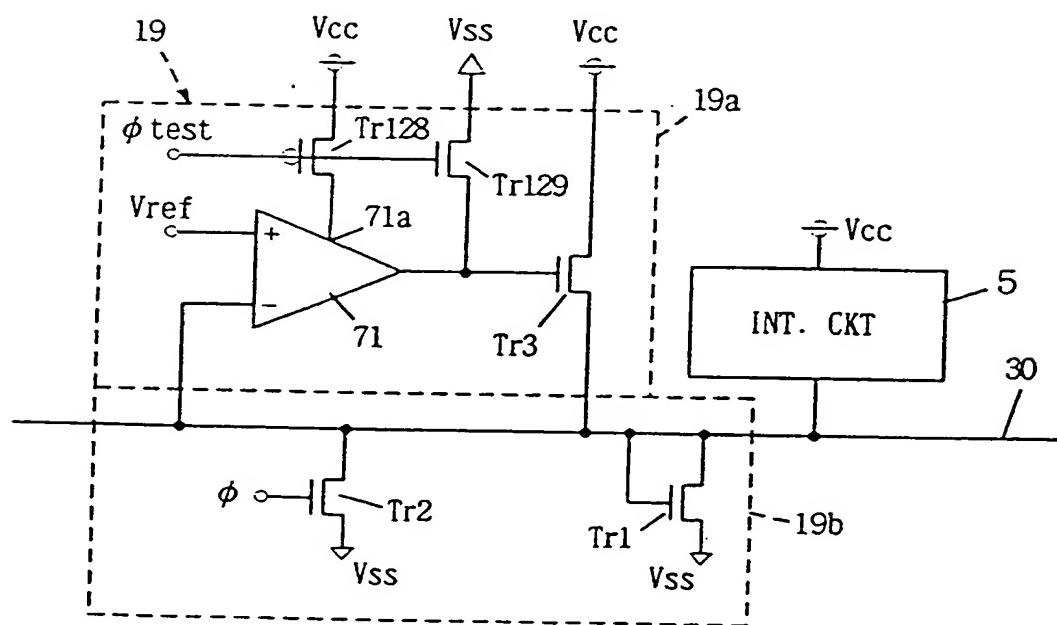




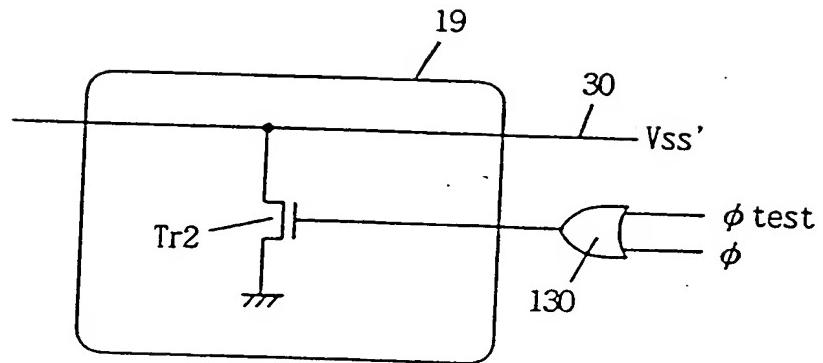
F I G. 84



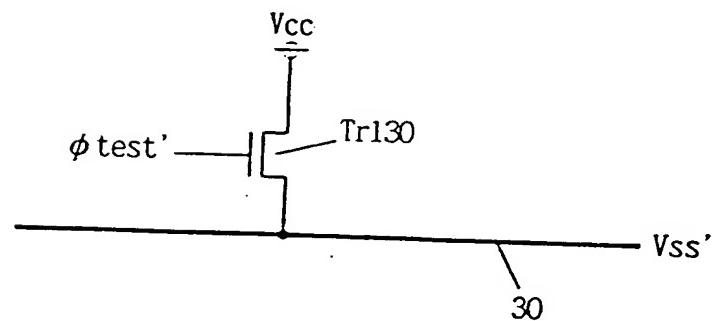
F I G. 85



F I G. 86



F I G. 87



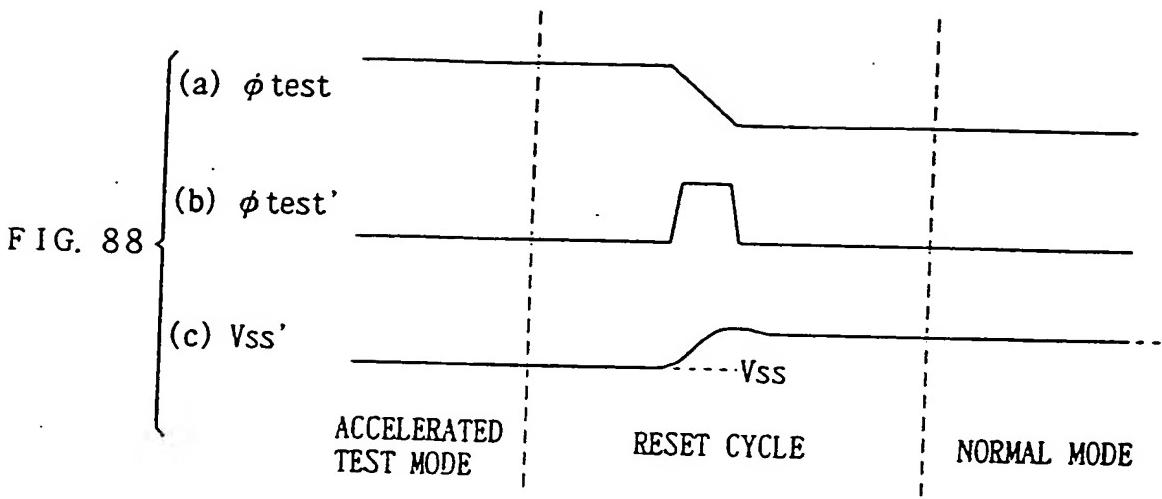


FIG. 89

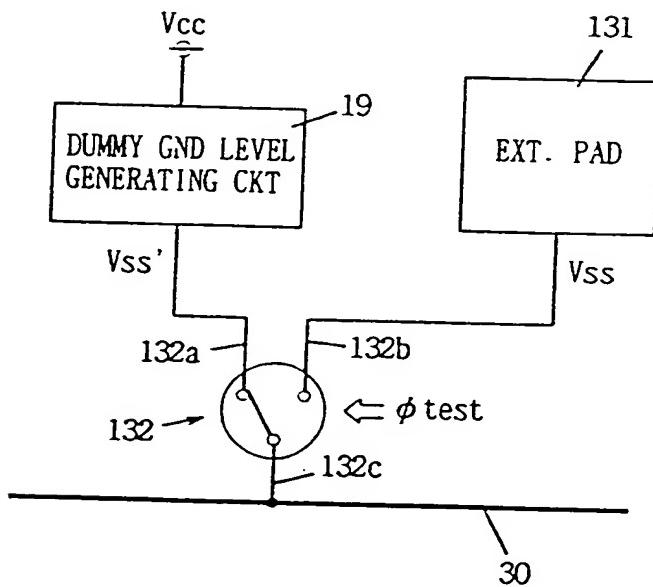
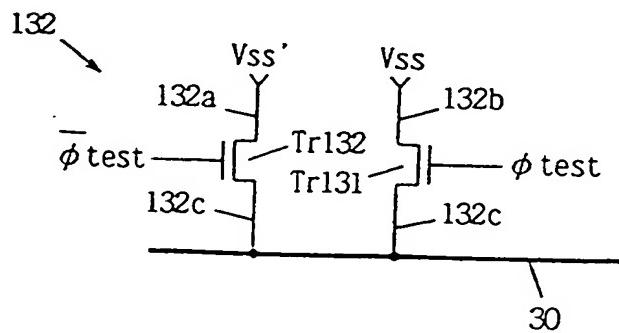
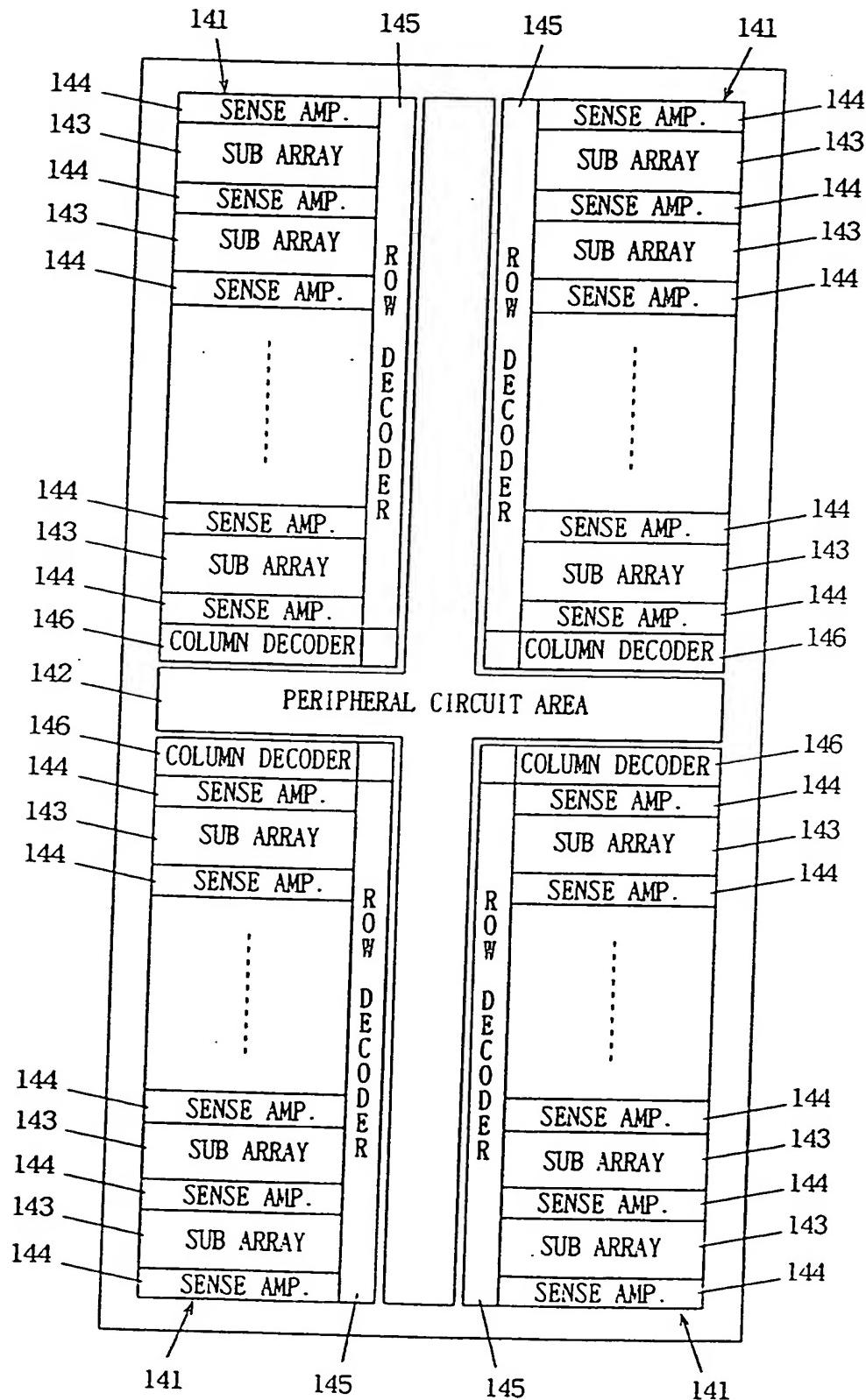


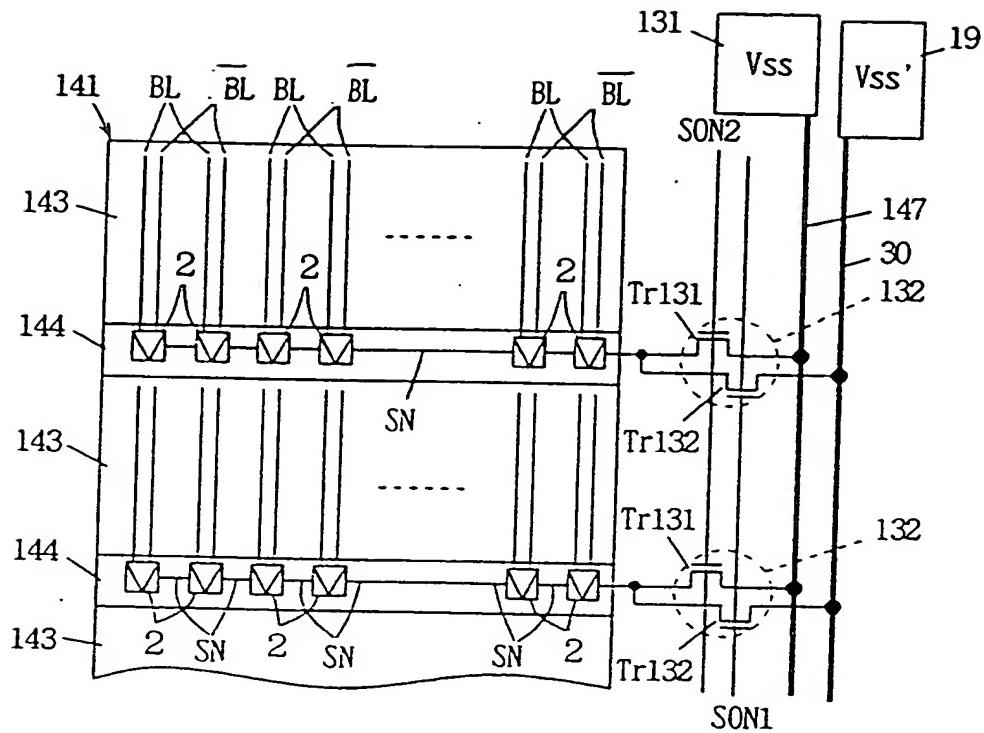
FIG. 90



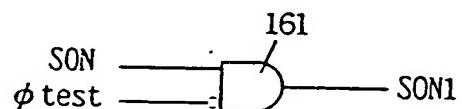
F I G. 91



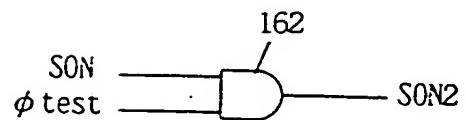
F I G. 92



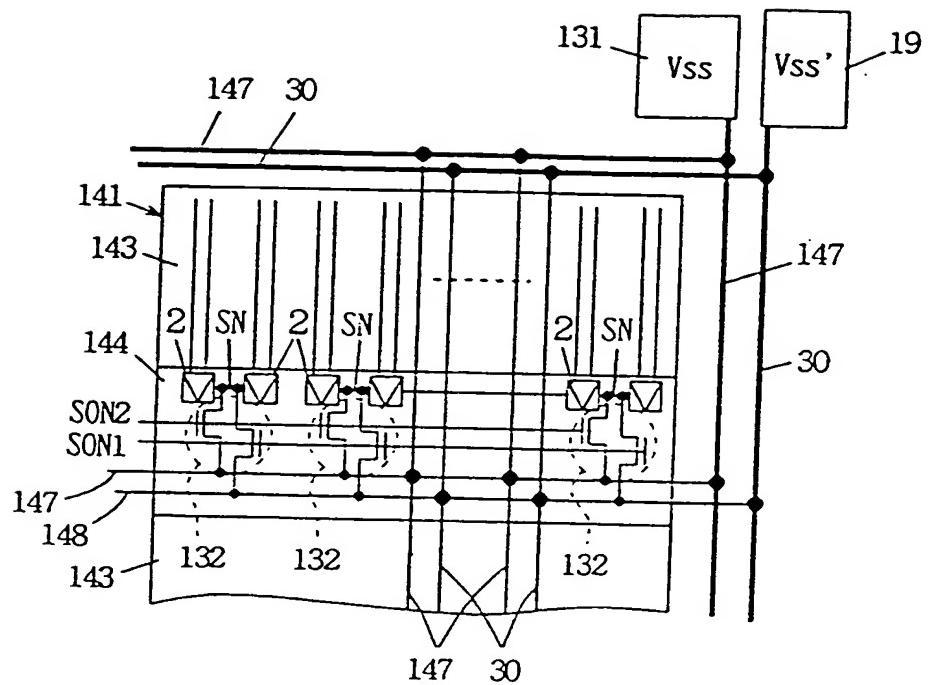
F I G. 93



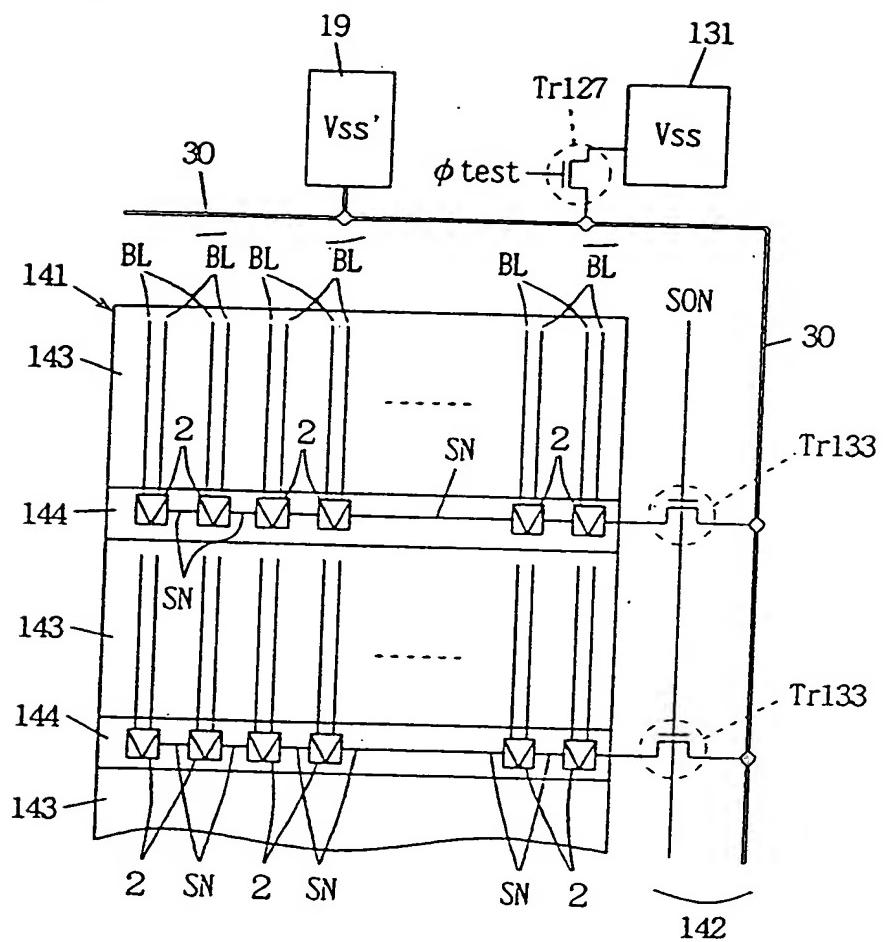
F I G. 94



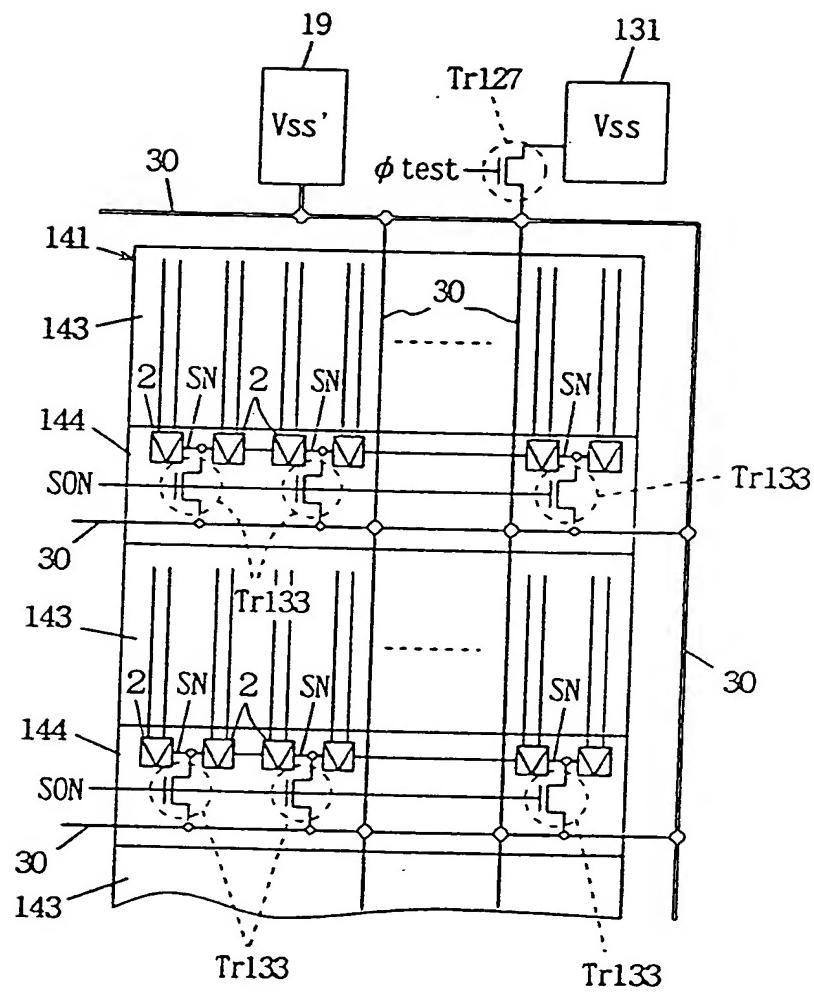
F I G. 95



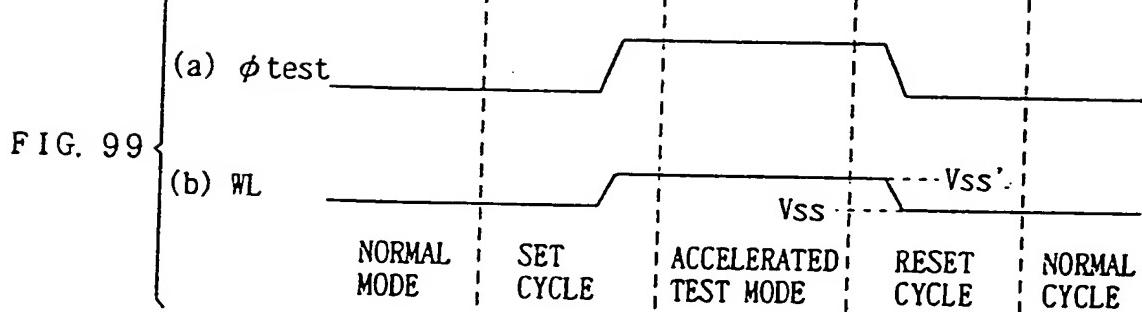
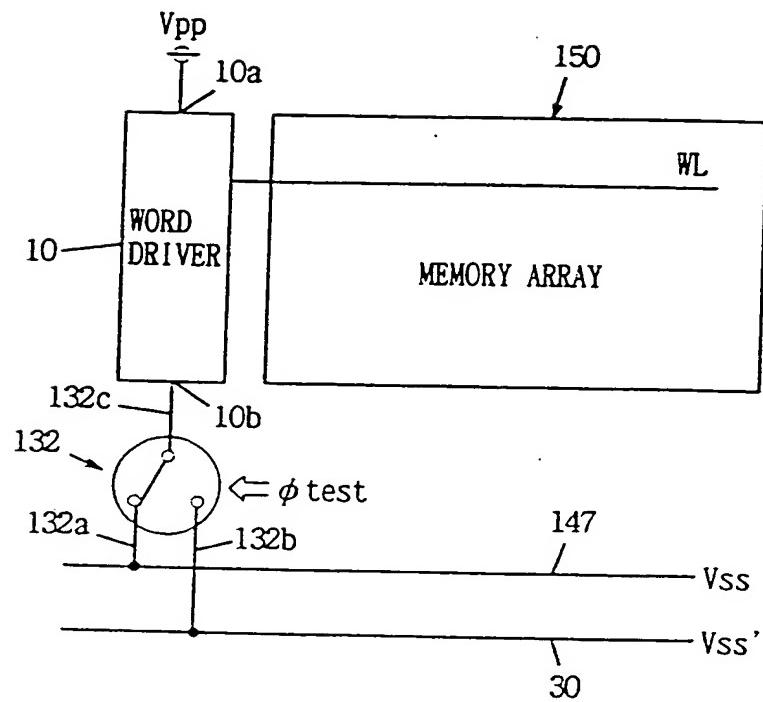
F I G. 96



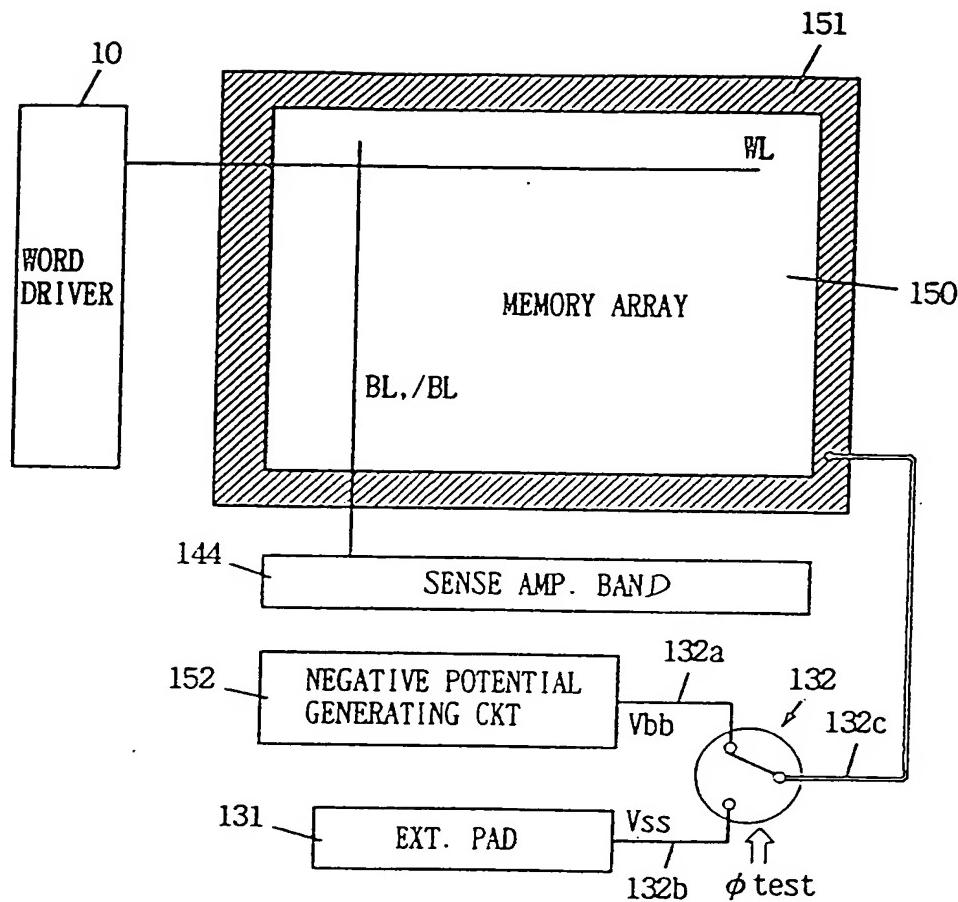
F I G. 97



F I G. 98



F I G. 1 0 0



F I G. 1 0 1 PRIOR ART

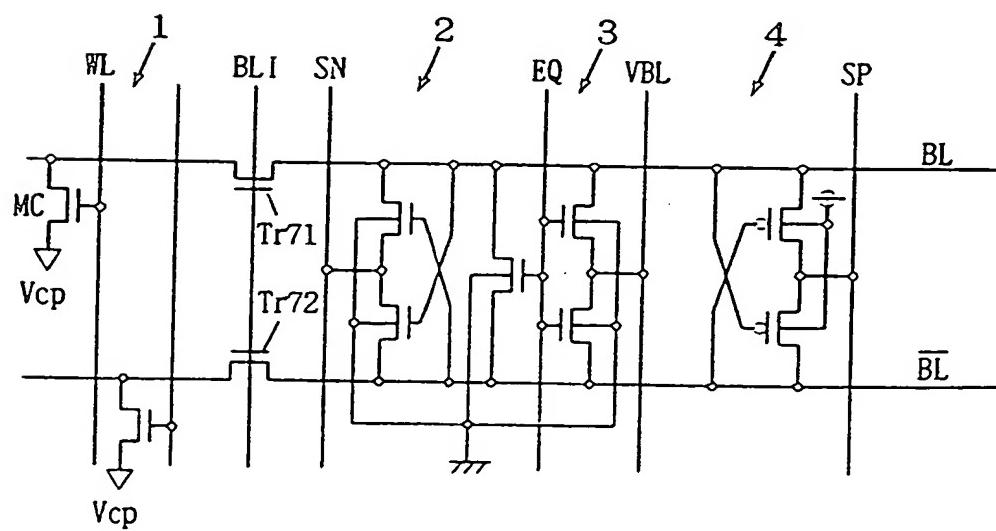


FIG. 102
PRIOR ART

